



Real-Time Intelligent System Design

★入选2020年校级研究生课程思政建设项目
★入选2021年校级研究生课程思政示范课程

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Class versions, WHICH ONE?

- ① English Slides + English Presentation?
- ② English Slides + Chinese Presentation?
- ③ Chinese Slides + English Presentation?

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One question of philosophy of science and technology

Scientist OR Engineer?

this linear model of innovation is in fact how things work. For example, About.com, an advertising-funded website centered around articles on a huge variety of subjects, collects reader commentary. On the question of "Engineer vs Scientist - What's the Difference?" some of the reader answers are:

Scientists are the ones who create the theories, engineers are the ones who implement them. They compliment [sic] each other...

Science is a lot of high level theory and engineering is implementation and optimization.

Engineers deal with math, efficiency and optimization while Scientist [sic] deal with "what is possible."

Engineers trained [sic] for Using tools, where Scientists are trained for Making them.

Scientists develop theories and work to verify them, Engineers search in these theories to "optimize" things in real life.

A scientist invents a law and an engineer applies it.

Scientist for invention of new theories [sic], Engineers for applying those theories for piratical [sic] applications.

Question: What is an engineer?

Answer: An optimist empty. An engineer is a glass half full.

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Science VS Engineering

- **What's Science? Engineering?**
 - [S] Discover from nature, [E] Human-made?
 - [S] Theory research, [E] Design & implementation?
 - [S] Innovation, [E] Application & Optimization?
 - [S] Superior, [E] Hard work & Inferior/LOW?
 - [E] is the LITTLE SISTER of [S]?
- **Wiki item**
 - **Engineering** is the application of mathematics, empirical evidence and scientific, economic, social, and practical knowledge in order to invent, innovate, design, build, maintain, research, and improve structures, machines, tools, systems, components, materials, and processes. (2016)
 - The term Engineering is derived from the Latin ingenium, meaning "cleverness" and ingeniare, meaning "to contrive, devise."

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Questions about our course

- What does **"REAL-TIME"** mean?
 - Please summary and write down
- What's **"INTELLIGENCE"**, especially with digital technology?
 - Please summary and write down
- Why do we focus on **"SYSTEM"**?
 - Please summary and write down

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Contents

- Overview
- Real - Time Intelligent System Structure and Method
 - Real - time operating system mechanisms (Resources , Schedule), taking VxWorks for example , Real-time Linux;
 - (Polymorphism) Adaptive scheduling
 - Real - time network mechanism
- The Topic Discussion of Intelligent Method
 - Big data and Machine Intelligence
 - Intelligent Sensor
 - Intelligent Circuit, Evolvable Logic, Polymorphic Calculation (*Improve calculation performance in parallel*)
 - Fuzzy Control, Neural Networks , Evolutionary Computation, Target Identification
 - Markov Chain and Its Model
 - Intelligent Internet of Things
- Development Method Theme
 - synchronous programming languages
- Simulation and Verification
 - How to verify real-time, How to verify intelligence, Monte Carlo Method
 - Semi - Digital and Semi - Physics
- Case study 1: Intelligent Industrial Control
- Case study 2: Unmanned Aerial Vehicle
- Case study 3: Intelligent Transportation

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- **Objective**
 - To discuss the composition, characteristics, modeling and design method of real-time intelligent systems from a system perspective on the basis of learning embedded computing technology, network communication and artificial intelligence.
- **Class Mode**
 - Theoretical Explanation + Topic Discussion + System Design
 - Each group – One Project – One presentation – Reports
- **Assessment Method**
 - Project: 50% + Topic Discussion: 30% + Class: 20%

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- **Reference Material**
 - 嵌入式系统体系、原理与设计, 张凯龙, 清华大学出版社, 2017;
 - 智能系统: 结构、设计与控制, Meytel A.M, 翻译版, 电子工业出版社, 2005;
 - *Real-Time Systems and Programming Languages (4th ed.)*, Alan Burns and Andy Wellings, Addison-Wesley, 2009;
 - 实时系统, Jane W.S.Liu, 翻译版, 高等教育出版社, 2003;
 - *Journal of Real-Time Systems*, Springer US, 1989-2014;
 - *Embedded Systems & Robots*, Subrata G., Cengage Learning, 2009;
 - 智能控制 (第2版), 刘金琨, 电子工业出版社, 2012;
 - 计算智能, 黄亮伟等, 科学出版社, 2010;
 - 人工智能控制, 蔡自兴, 化学工业出版社, 2005;
 - 机器学习, Mitchell T.M., 翻译版, 机械工业出版社, 2008;
 - IEEE: <http://www.ieee.org/index.html>
 - 智能时代—大数据与智能革命定义未来, 吴军, 中信出版集团, 2017

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Online resources: coming

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- **Previous Students' Works**
 - Yang Ansheng ,et al. Inverted Pendulum Based on Fuzzy Control
 - Su Hang, et al. Vehicle License Plate Recognition
 - Zuo Panfei ,et al. Quad-rotor Unmanned Aircraft
 - Zhao Hua ,et al. Visual Recognition Robot
 -

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- **2017 students' work video**
- **2019 students' work video**

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Outline

- Development Background Extension
- The Intelligence of Embedded System
- A Variety of Intelligent Systems
- Research on Intelligent Embedded Computing
- Embedded Intelligent Research in Europe
- Our Related Work

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Part I: Hello Embedded 1.0
Background Extension

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麦克斯韦 (1831-1879)
英国物理学家、数学家



1871年麦克斯韦的提出产生了信息熵与计算的概念
《论热能》



开尔文 (1824-1907)
英国物理学家、数学家、电学家、发明家



1876年研制出潮汐预报装置
1882年研制出非光带用"calculator"一词




图灵 (1912-1954)
英国数学家、逻辑学家、计算机科学之父、人工智能之父



图灵机

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克劳德·艾尔伍德·香农 (1916-2001)
美国数学家、信息论的创始人


$$H = -\sum P_i \log P_i$$

《通讯的数学原理》、《噪声下的通信》

- 信息熵与信息论
- 符号逻辑和开关理论，奠定了数字电路的理论基础。



约翰·冯·诺依曼 (1903-1957)
美国籍匈牙利数学家、计算机科学家、物理学家、现代计算机之父、博弈论之父




冯诺依曼体系结构

- 采用二进制制作为数字计算机的数制基础
- 在计算机内部的存储器中存放程序

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- From the assumption of Kelvin to the emergence of the automatic computing device
 - The development of computing technology stems from the expectation of efficiency
 - In the 19th century, the first automatic computing device was the tide predictor, which was designed by Kelvin. It was a mechanical device that could calculate the tide, but it was not a computer.



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- In 1876, Kelvin designed and implemented a tidal device for forecasting tide;
- Kelvin first used the word "Calculator" in an academic report in 1882;

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Differential Analyzer (微分分析仪)

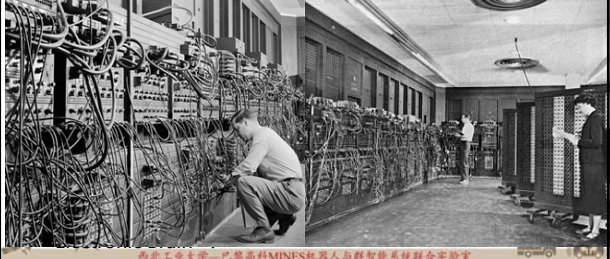
- Developed by Van Geneva Bush in 1931. It was a typical implementation of Kelvin's assumption and the first mechanical computer that was used to solve differential equations;
- Electronic components are further used to replace certain mechanical parts, and computing device is optimized to form the "Rockefeller Differential Analyzer 2".



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Electronic computer technology provides high efficiency, high precision and complex computing ability to replace part of the work of "man"!

- Ballistic trajectory and Fire Table calculation is very important to improve the hit rate;
- A fire table has more than 3000 parameters. For a 60 second range of ballistic time, a person



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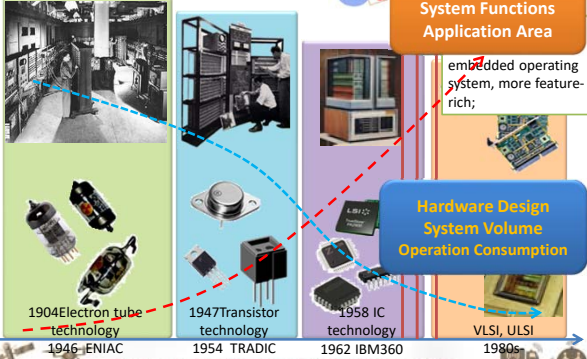
Digital Computing Technology

Computing Capability
Peripheral Component
Software Capability
System Functions
Application Area

embedded operating system, more feature-rich;

Hardware Design
System Volume
Operation Consumption

1904 Electron tube technology
1946 ENIAC
1947 Transistor technology
1954 TRADIC
1958 IC technology
1962 IBM360
VLSI, ULSI
1980s



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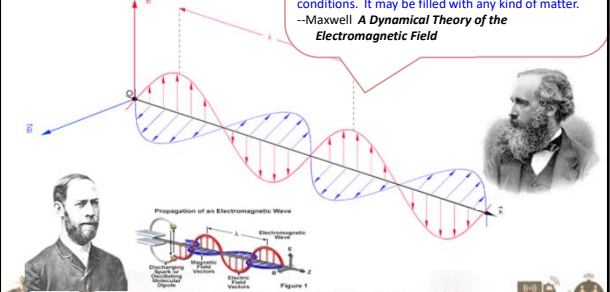
Foundations of Electromagnetism

We can scarcely avoid the inference that light consists in the transverse undulations of the same medium which is the cause of electric and magnetic phenomena.

Maxwell On Physical Lines of Force

The electromagnetic field is that part of space which contains and surrounds bodies in electric or magnetic conditions. It may be filled with any kind of matter.

Maxwell A Dynamical Theory of the Electromagnetic Field



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Tip : Stand on the shoulders of giants with awe and the sense of mission!

Communication and Network

In 1962, Professor Licklider proposed the network which links all computers together, as well as automatic and reliable information exchange!

Communication and network technology realize the interconnection of computing devices, as well as automatic and reliable information exchange!

1831 1837 1873 1876 1888 1895 1928 1938 1948 1950年代

电磁感应 莫尔斯电报机 电磁场理论 贝尔电话机 发射电磁波 无线电报 采样定理 贝尔移动电话 香农定理 微波通信

In 1962, established ARPANET was




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The emergence and application of embedded computing technology achieve the integration of "mechanical + electronic + software", so that equipment has a digital capacity, with a certain "thinking and control" ability, partially replace, even beyond the role of "human" factor;

Key notations

- Real-time → Physical rules
- Intelligence → Biological characteristics
- System → Organic aspect: thought and ability



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未来已来，智能系统的发展与挑战！

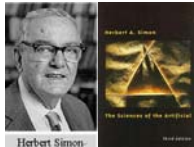
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Part II : Some Typical Intelligent Embedded Systems

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Artificial & Natural

- **List what are natural**
 - 自然物, things the nature provides.
- **List what are artificial**
 - 人造物、人工制品
 - political systems, economies,
 - engineered artifacts, and administrative organizations
- **Think about what is artificial**
 - The thesis is that certain phenomena are “**artificial**” in a very specific sense: *they are as they are only because of a system's being molded, by goals or purposes, to the environment in which it lives.* — 《The Sciences of the Artificial》, Simon, 1996
 - Herbert A. Simon, winner of both the Turing Award in computer science and the Nobel Prize in economics



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Design and Discovery

- In contrast to Simon's “sciences of the artificial,” the “**sciences of the natural**” study what nature has given us. **The goal** is to uncover the “**secrets of nature**,” presupposed to exist disembodied, independent of humans.
 - Physical, Chemical, Celestial Laws.....
 - Lao-zi's DAO (道), or Plato's Form (型相) → Good (善)



- 《道德经》：道反映自然规律、个体修行，是宇宙本源；德反映世界观、方法论、处世之法等，体现社会法则；
- 柏拉图认为世间的一切皆为理念界的事本，只有理念界才是真实的。

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Plato's Allegory of the Cave



The School of Athens
Plato and his student, Aristotle

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An example

- **A surprised Teddy**
 - Natural or Artificial?
 - Discovery or Design?

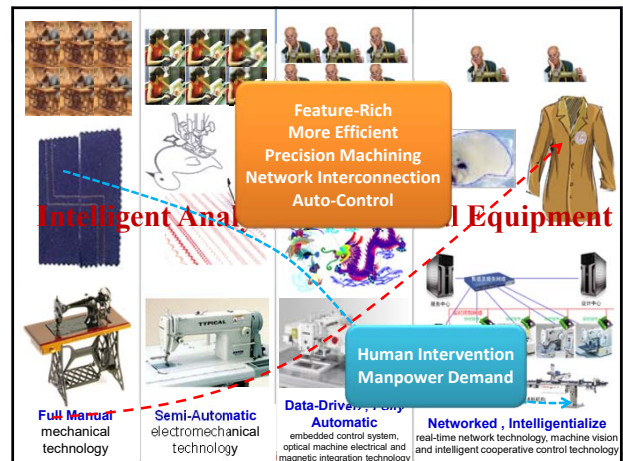


- <https://tech.sina.com.cn/roll/2020-09-17/doc-iivhvpwy7308538.shtml>

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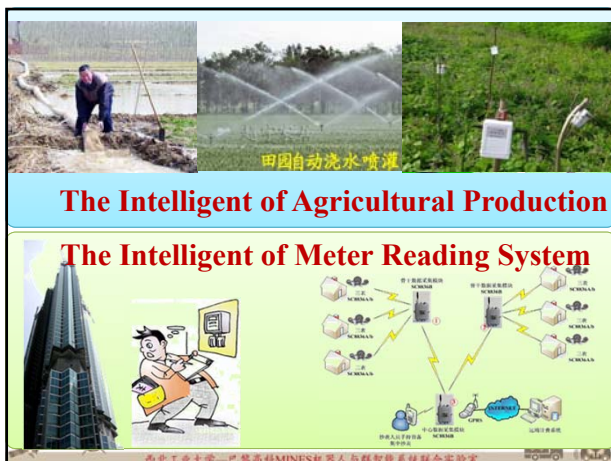
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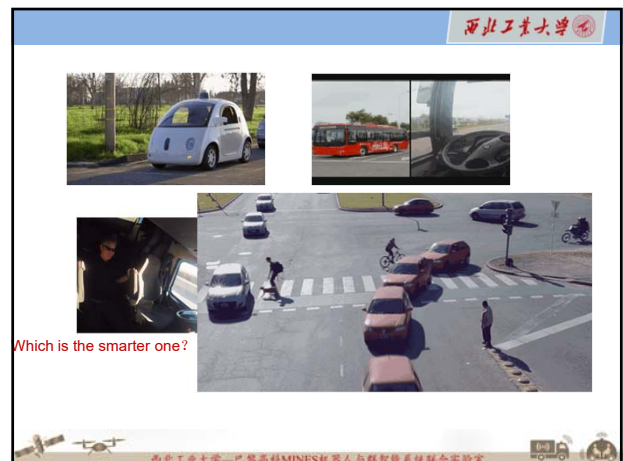
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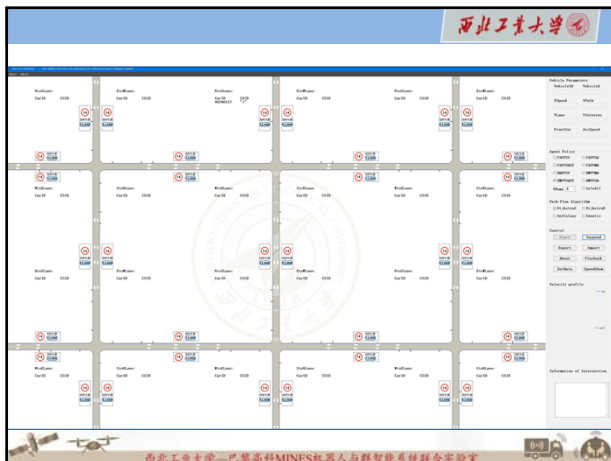
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Questions

- If any example here is smart, but without time constraint?
 - Please summary and write down
- What's the general features of these smart systems?
 - Please summary and write down

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A Variety of Intelligent Systems

Era of the Intelligence

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Embedded Intelligent Research in Europe

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Part III : Embedded (Real-time) Intelligent Technology

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- **Definition of Embedded System**
- **Connotation** : Based on embedded computing technology, effectively improve the equipment

The Definition of Barr Group—An International Research Organization

An embedded system is a combination of computer hardware and software, and perhaps additional mechanical or other parts, designed to perform a dedicated function. In some cases, embedded systems are part of a larger system or product, as in the case of an antilock braking system in a car.

(译文：嵌入式系统是一组计算机软、硬件的综合体，还可以附属机械或其他组件，被设计来执行特定的功能；在很多情形下，嵌入式系统是大系统或产品的一个组成部分，例如汽车中的防抱死系统（ABS）)

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多选题 1分

此题未设置答案，请点击右侧设置按钮

Which devices are embedded systems? 提交

A HDD	E Engine Controller
B PDA	F Radar
C Mobile Phone	G Heart Pacemaker
D Flight-Management Computer	H HIL Simulator

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• **General Definitions for Embedded Systems**

– **Technically**

- Embedded system is a dedicated computer system that is application-centric and based on computer technology, software and hardware can be tailored to

An embedded system is part of a large system which performs a particular function;

- An embedded system can include multiple embedded subsystems.

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• **Brief Summary of the Connotation of The Concept**

- Embedded system technology is based on computer technology and combines with domain technology;
- A dedicated computing system that oriented application customization and development of software and hardware.

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• **The “Embeddable” Development Process of Computers**

– **Embedded “1.0” : Calculation Function Embedded**

- The computer has a certain degree of specificity, which

computer Whirlwind for controlling flight simulator and its magnetic core memory

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– **Embedded “3.0” : Polymorphous Networked**

- Various lightweight network protocol stacks suitable for embedded applications have been developed. Hardware, operating systems provide network interfaces and services, and more and more applications are connected to the network.

– **Embedded “4.0” : Deep Integration of Information (Systems) with Physics (the World)**

- The computing device can interact autonomously with the physical world in a certain degree through sensors and actuators, showing the complex computing characteristics of intelligent function, information system and physical world.

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Distinction of several concepts

• **Classical Embedded System VS Cyber-Physical System (CPS)**

- 环境感知能力、计算能力、环境交互能力、服务能力、预测能力不断丰富和增强；
- 早期的数传控制、移动电话（前者）；
- 正在出现的智能驾驶、智能电话（后者）。

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- **ES VS IoT**
 - 前者赋予物件“生命”，是前提；
 - 后者是网络化嵌入式系统互联形成的大应用系统。
- **ES VS Intelligent Systems**
 - 前者让物件数字化，是前提；
 - 后者融合综合感知、AI、大数据、云计算等技术，实现自主的行为、服务能力。

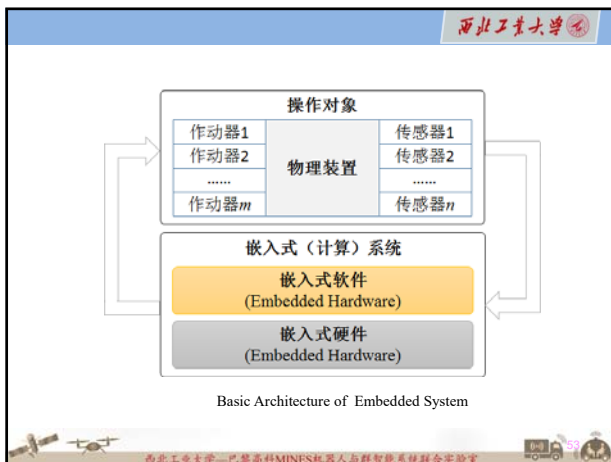



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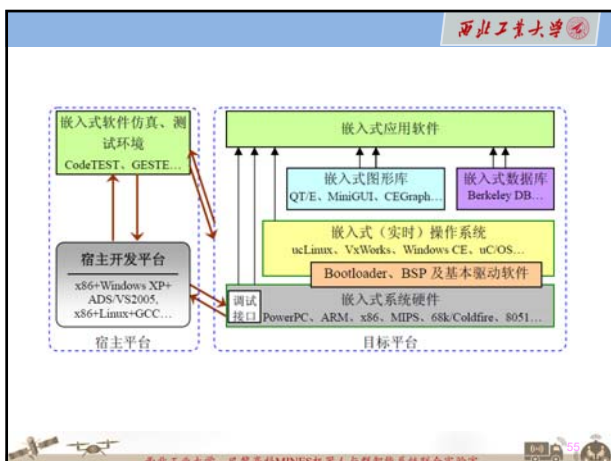
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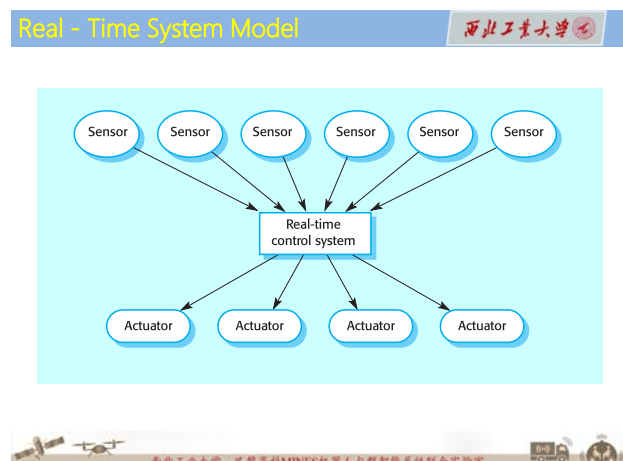
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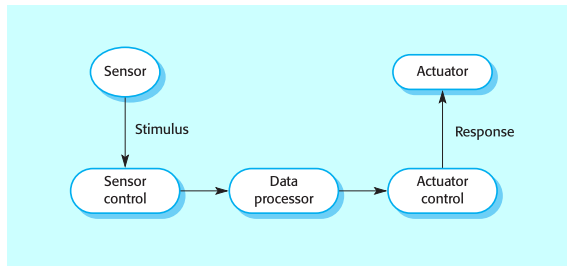
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Sensor/Actuator Processes

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About Model

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• What is a Model? List several models



• British statistician, George E. P. Box:

"All models are wrong, but some are useful."

The world should be creative!

Werner Karl Heisenberg: The uncertainty principle

"The world is uncertain!"

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Understanding "Real-Time" from the aspect of EOS

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The Connotation of "Real-Time"

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- The Sinking of The Titanic
- Missile Interception Failed
- Mars Polar Lander Crashed



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- When people use computers to solve problems, they focus mainly on the correctness of the calculations and do not mind the time;
- In a considerable number of applications, the correctness of the results is closely related to the computation time, and even time is more

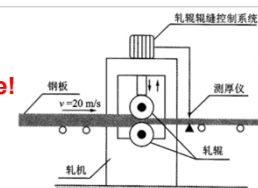
The calculation of the system must produce the correct result, called **Logical or Functional Correctness**; the calculation of the system must be completed within a predetermined time, called **Timing Correctness**.



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- The rolled steel plate passed at the speed of 20m / s, and the gauge does not meet the requirement of thickness. Then the adjustment instruction is issued to the control system. If the control device needs 100ms to control the machine to meet the requirements, is it timely?
- If the total length of the steel plate is 100 m, approximately 2 m of waste will be produced during the adjustment period; Require rejection rate not more than 3%, in time; The requirement of the reject rate becomes less than 1.5%, not in time.

Real-time is also relative!



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– **What is a real-time system?**

- A very fast computing system?
- A system with very small clock cycle? 作业要求你多快处理，你就能多快处理的系统。
- Single tasking or multitasking?

特点：如果逻辑和时序出现偏差将会引起严重后果
分类：软实时系统和硬实时系统
软实时系统的宗旨是使各个任务运行的越快越好
硬实时系统各个任务不仅要执行无误而且要做到准时
大多数实时系统是两者的结合，且多数实时系统又是嵌入式

就是处理时间要求非常严格的系统，以嵌入式操作系统居多。

银行，机场，重要的环境监测，等等都用到了实时系统

实时系统就是CPU执行任务时总是一个一个的执行，只有在执行的问题完成了之后，才交出CPU，这种方式的速度很快，但是去不怎么好用，DOS就是这种工作方式；
与这种工作方式相对的就是分时系统，WINDOWS就是分时系统工作方式，CPU被分成很多个芯片，同时执行多个任务，当然，这中间占用芯片的大小我就搞不懂了：）我没有深研过操作系统

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– **The definition of POSIX 1003.b**

- Refers to a system that provides the required level of service within a defined response time

– **The definition proposed by Donald Gillies**

- A real-time system means that the correctness of the calculation depends not only on the logical correctness of the program, but also on the time it takes for the result. If the time constraints of the system are not met, a system error will occur.

– **A computer application system that can respond to external random events in a timely manner and complete the processing of events at a sufficiently fast speed (within a given time) .**

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– **Basic Characteristics**

- Time constraints: hard real-time, soft real-time
- Predictability
- Reliability
- The interaction of the external environment: control, CPS

– **New Features**

- 多任务类型: periodic, nonperiodic, occasional, anytime;
- The complexity of constraints: time constraints, resource constraints, execution sequence constraints, and performance constraints;
- Have the characteristic of 短暂超载: Caused by component aging, dynamic variations of environment and the overloading of application scale.

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• **Time Attributes of One Task**

- Release time (释放时间), the time when the task is ready; If all tasks are ready when the system is started, the release time is 0;
- Deadline (截止期), the time when the task must be completed; If the deadline is infinite, it means that the deadline does not exist;
- Relative deadline (相对截止期), the event interval between the release time and the deadline, and the task should be completed within that time period;
- Response time (响应时间), the time interval between the completion and completion of the task, equal to the completion time release time";
- Tardiness (延迟时间), If the completion time does not exceed the deadline, the time value is 0, otherwise it is equal to the completion time minus the deadline;
- Lateness (滞后时间), which is equal to the completion time minus the deadline, and the value can be positive or negative.

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• **Task Scheduling Characteristics**

- Feasible schedule (可行的调度), For each task with a deadline requirement, it can be completed before the deadline as long as it is activated at (or after) the release time;
- Schedulable (可调度性), For a scheduling algorithm, a group of tasks always have a feasible scheduling scheme; if all the tasks in a system are schedulable, it can be said that the system is real-time;
- Optimal Schedule (调度优化), As long as it exists, the scheduling algorithm can always find this feasible scheduling sequence;
- Miss rate (错失率), The proportion of tasks that have been executed but completed beyond the deadline;
- Loss rate (丢失率), The proportion of discarded tasks;
- Invalid rate (失效率), which is equal to "miss rate + loss rate".

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Hard Real-Time

- **The deadline must be met**
 - The deadline for a task must be met → **Hard Real-Time Task**;
 - If any deadline is wrong, it will cause a system error;
 - Therefore, it is necessary to verify whether the deadline for each task can be met.
- **Hard Real-Time System**
 - All tasks with deadlines, their deadlines must be satisfied, that is to say all real-time tasks are hard real-time tasks → **Hard Real-Time System**;
 - For example: flight control, nuclear power plant control

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Soft Real-Time System

- **Soft deadlines are allowed to be missed**
 - When a task's deadline is missed, it does not cause serious consequences → **Soft Real-Time Task**;
 - The longer the completion time exceeds the deadline (the longer the lateness), the worse the credibility of the logical result is → Statistical Thinking;
- **Soft Real-Time System**
 - There are some tasks that are soft real-time tasks for the system → **Soft Real-Time System**;
 - For example, multimedia applications etc.

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Concept Differentiation: which are correct?

- Embedded operating systems are not necessarily real-time operating systems, such as μ CLinux, Windows XP/E;
- A real-time operating system is not necessarily an embedded operating system, such as Linux+RT-Linux, Windows+RTX;
- A real-time system cannot be constructed based on time-sharing operating systems;
- A system built on a real-time operating system is not necessarily a real-time system, unless the real-time service mechanism is used and the schedulability of the time constraints is guaranteed;
- Generalized real-time systems are not necessarily embedded systems, for example, real-time systems can be built on PC;
- Embedded systems are not necessarily real-time systems, such as mobile phones, routers / switches, medical devices, etc..

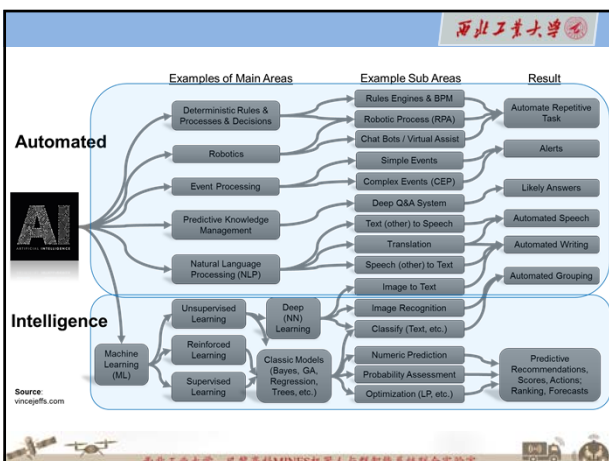
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Part IV: About Intelligence

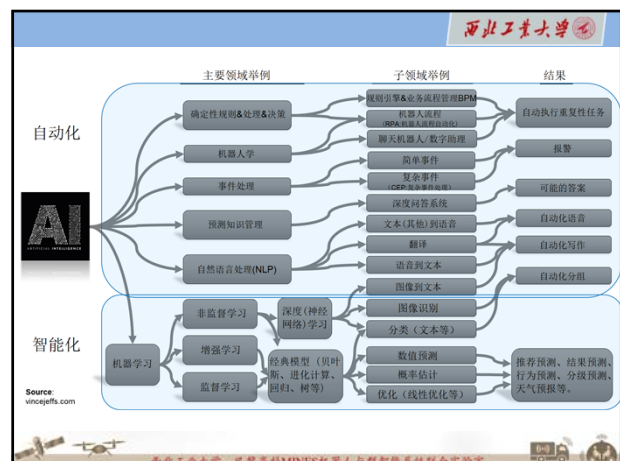
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- The process from sensation to memory to thought is called "wisdom". The result of wisdom produces behavior and language, and the process of expressing actions and language is called "ability", the combination of them is called **intelligence**;
- **Intelligence is a process of combining the smart and the ability;**
- **Information domains: Self-Adaption, Autonomy, Intelligence, Evolution ...**

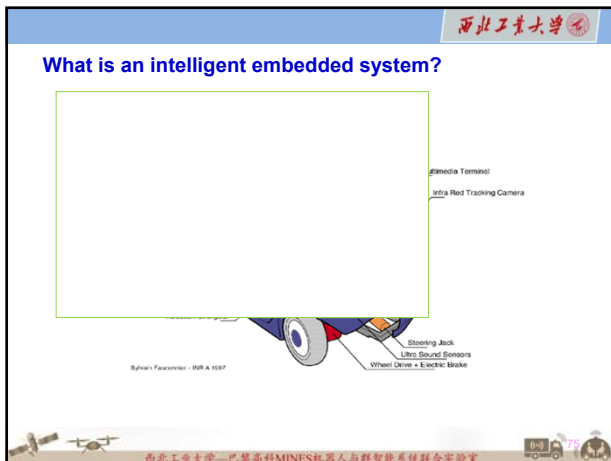
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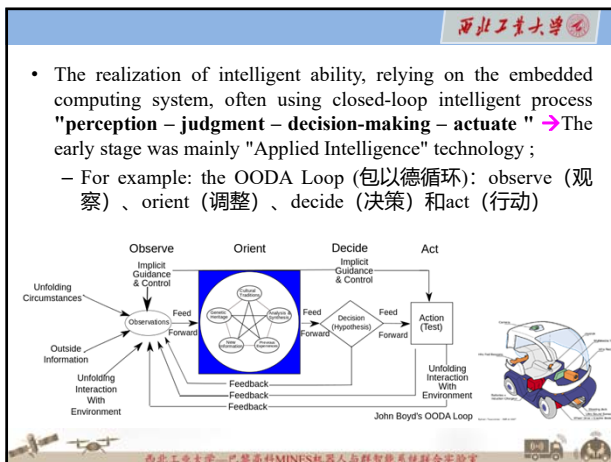


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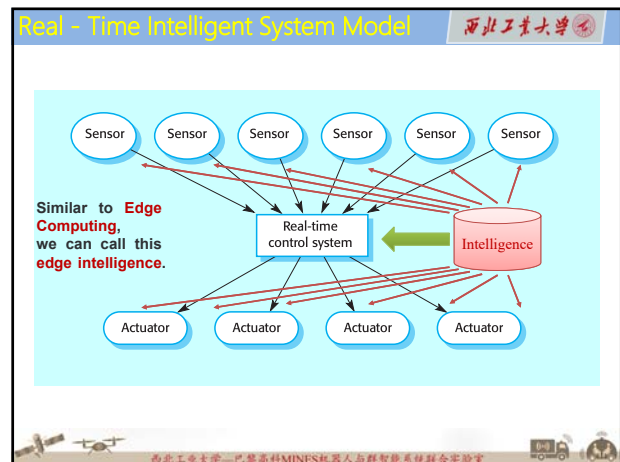
What is an intelligent embedded system?

- It is a new embedded application system with the autonomous ability, and it is the advanced form of embedded system development;
- **Connotation:** On the basis of embedded system technology, through the deployment of **intelligent hardware** and **software**, the computing system can "more" complete the device running environment awareness, judgment, decision-making and adaptive control;
- Embedded software is the soul of intelligence!
- Different field applications have different "intelligent" features, and the corresponding theories and techniques are different.

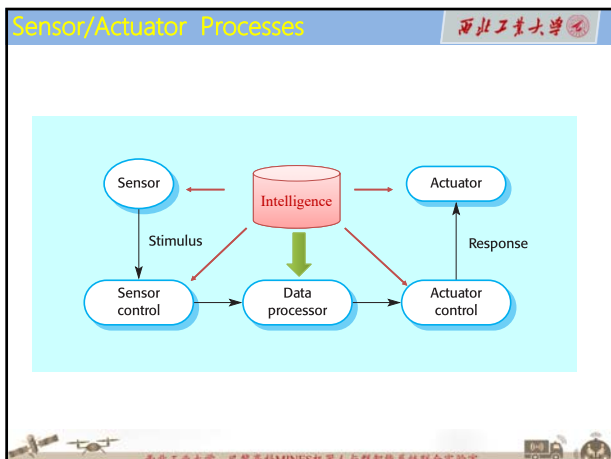
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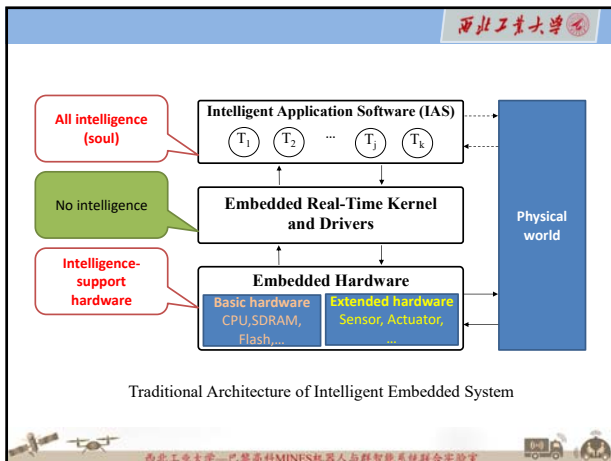


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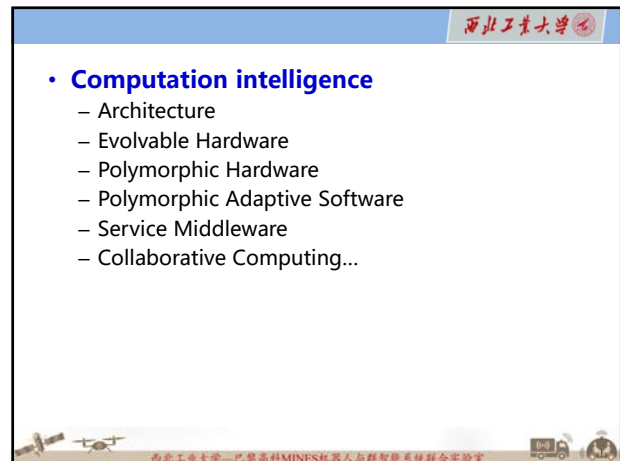
Application Intelligence

- **Objective:** Providing some kind of automation and intelligence for a specific application.
- **Related techniques**
 - Integrated sensing technology;
 - High performance embedded computing technology;
 - Pattern extraction and recognition;
 - Real-time intelligent processing and control method;
 - Communication and networking technology;
 - Optical Electromechanical Integration ,Micro-Electro-Mechanical System(MEMS) technology, etc.
 - Biotechnology ...

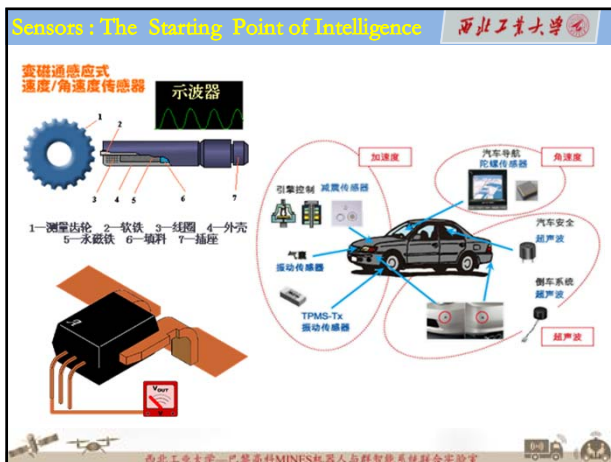
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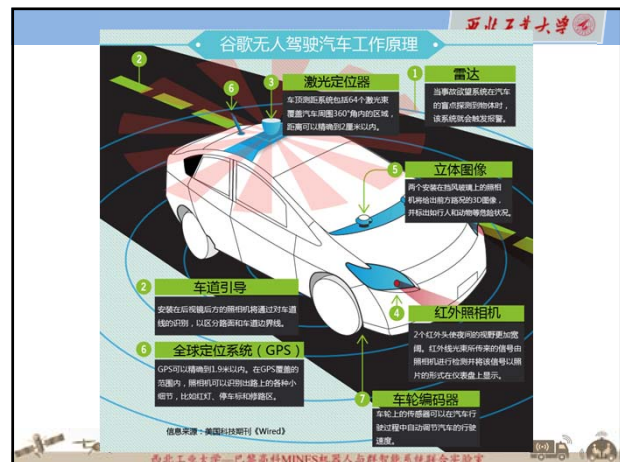
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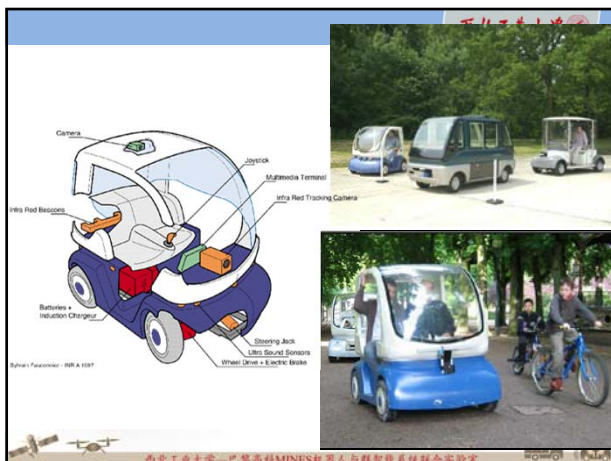
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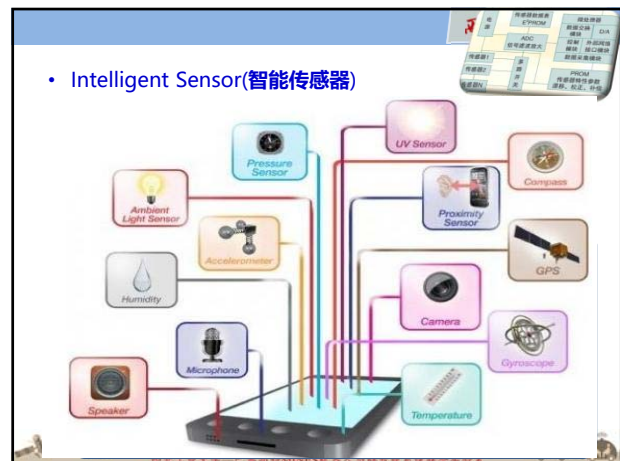
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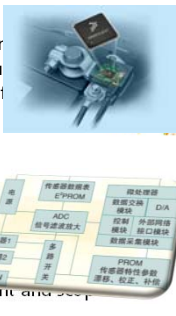
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– Function

- Information storage and transmission
- Self compensation and calculation function
- Self checking, self calibration and self function
- Compound sensing functions
- Integration of intelligent sensors

– Characteristic

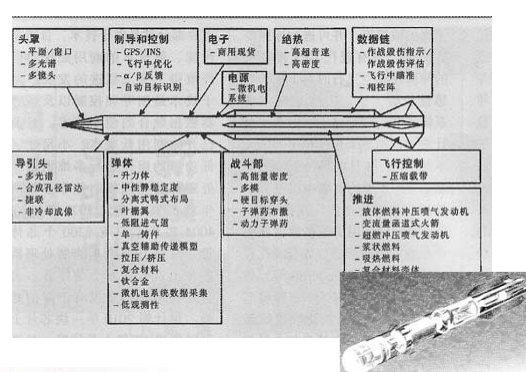
- Improve accuracy
- Improve reliability
- Improve cost performance
- Adaptive capabilities for measurement and test application



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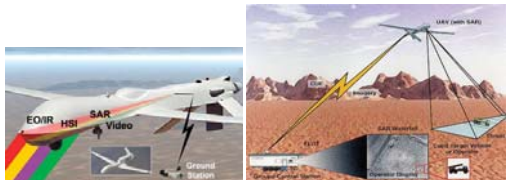
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– Multi-Sensor Data Fusion

- A large scale HPEC 6000 signal processing system with dozens of PowerPC 8640D/8641D processors based on RapidIO technology, can meet the data processing and data fusion of large UAVSAR, radar, photoelectricity, infrared, hyperspectral imaging and ground multi-target detection.



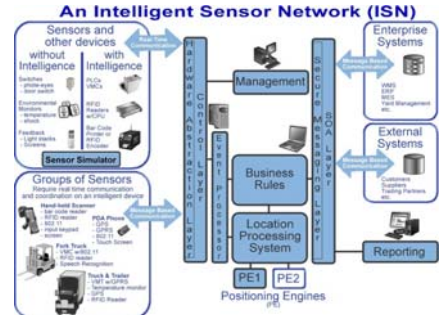
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西北工业大学

– Intelligent Sensor Network (ISN, 智能传感器网络)

An Intelligent Sensor Network (ISN)

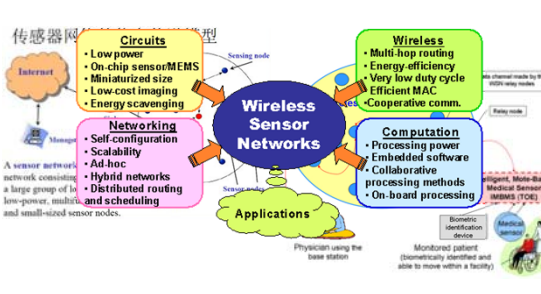


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– Wireless Sensor Networks (WSN, 无线传感器网络)




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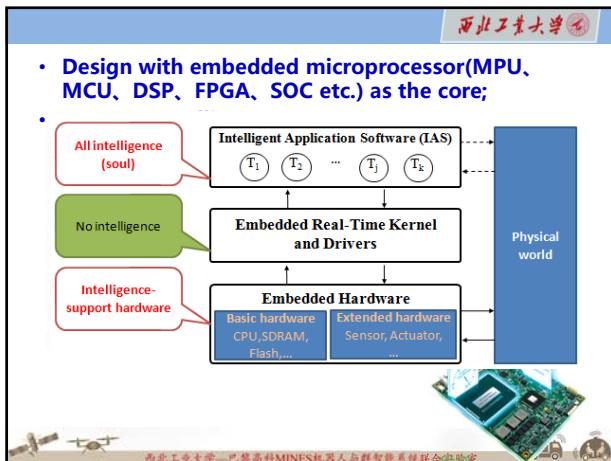
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Hardware: The Foundation of Intelligence

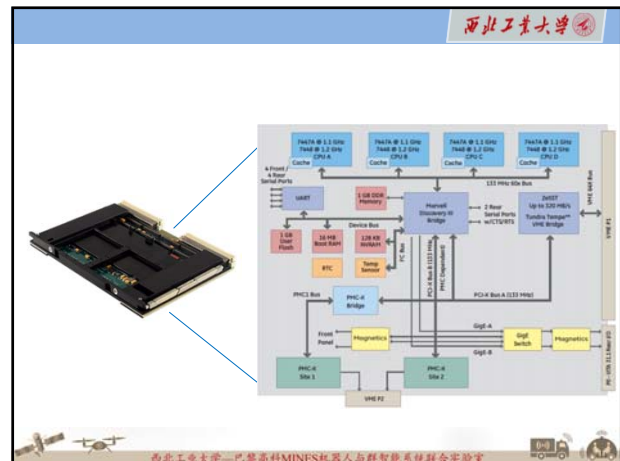


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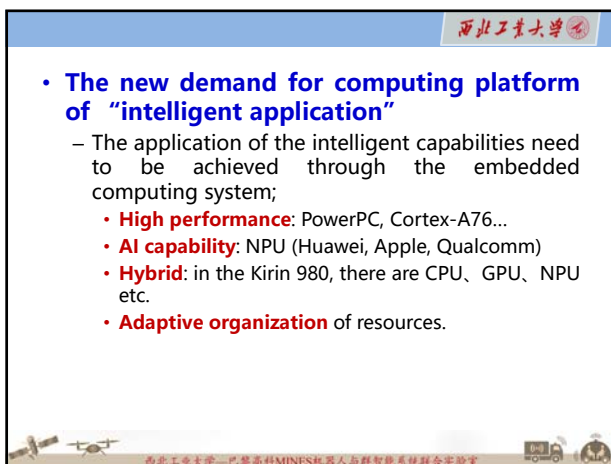
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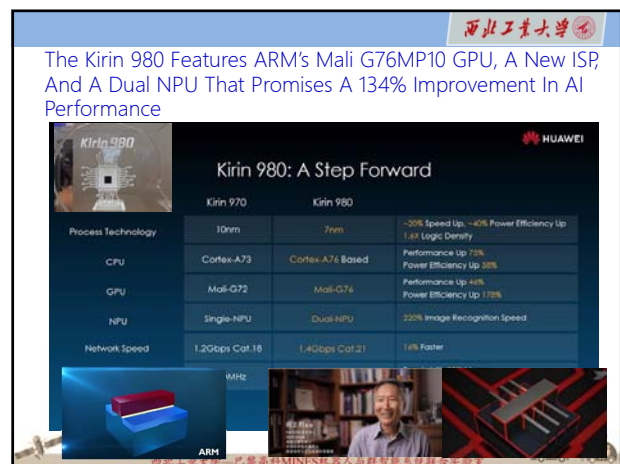
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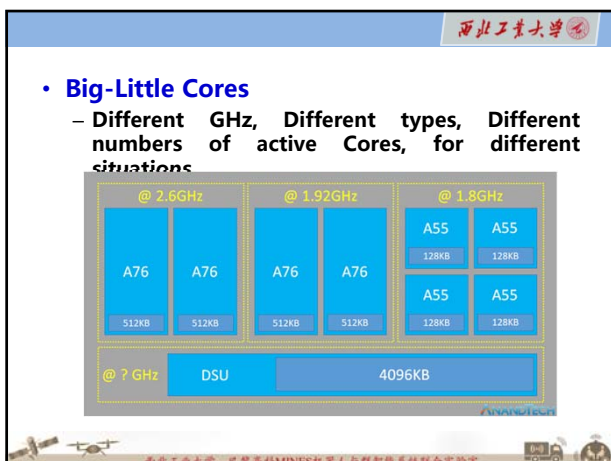
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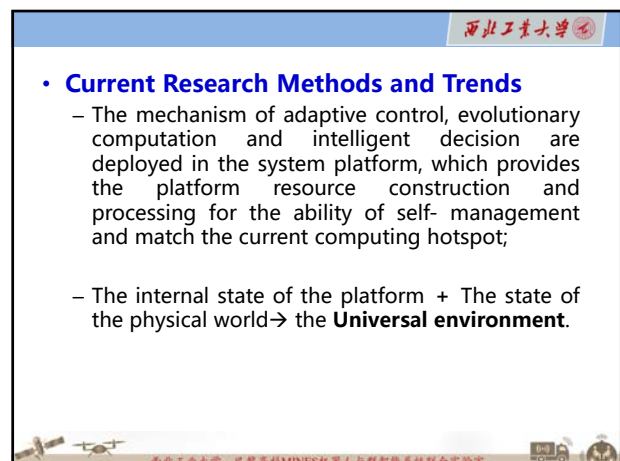
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The diagram illustrates the system architecture and flight profile of a solar-powered aircraft. The top part shows a 3D perspective of the aircraft's flight path, labeled with stages: Takeoff, Descent, Cruise, Morning Phase, Final Approach, and Landing. Below this, a 2D block diagram shows the aircraft's internal components and their interconnections:

- UHF Dipole Antenna** (Type A, Resonant 400 MHz, Impedance 100 Ohms)
- Structure** (Aluminum Extrusion, Supplier: CMC)
- Solar Panels** (Resonant 400 MHz, Supplier: CMC)
- Flight Control Computer** (Type A, Resonant 400 MHz, Supplier: CMC)
- Electrical Power Supply** (Type A, Resonant 400 MHz, Supplier: CMC)
- UHF Transceiver** (Type A, Resonant 400 MHz, Supplier: CMC)
- Quantizer** (Type A, Resonant 400 MHz, Supplier: CMC)
- Payload** (Type A, Resonant 400 MHz, Supplier: CMC)

Arrows indicate the flow of data and power between these components. The bottom right corner features a small inset image of the aircraft in flight, along with a logo for 'North China University of Aeronautics and Astronautics'.

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Colorful Hardware

---that changes during run-time

Further complexity arises from the introduction of *heterogeneous processing units* that feature more than one type of processing resource. Not only the borders between CPUs, DSPs and (recently) GPUs are blurring, but also the arrival of **reconfigurable computing units** (FPGAs, reconfigurable processors) in the embedded domain has increased the need for effective and efficient design, analysis and verification methods. Whilst many advances have been made in the area of compilers/synthesizers of individual processing resources (e.g., auto-vectorization on CPUs, inference of target-specific components on FPGAs), there is still a lack of support for heterogeneous design flows, especially in the real-time domain.

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• Reconfigurable Logic (可重构逻辑)

- PAL (Programmable Array Logic)
 - A programmable AND array and a fixed OR array, each OR gate of the OR array acquires input from a group of AND gates;
- PAL (Programmable Logic Array)
 - There is not only a programmable AND array, but also a programmable OR array;
 - The AND array is used to implement the product of the SOP (Sum of Product) expression, and the OR array is used to sum the product terms.

The diagram illustrates two types of reconfigurable logic: PAL and PLA. On the left, a schematic shows a grid of AND gates (programmable) and OR gates (fixed for PAL, programmable for PLA). On the right, a 3D block diagram shows input signals entering an AND array (labeled 'programming of product terms'), which then connects to an OR array (labeled 'programming of sum terms') to produce output signals.

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A 4 Bit BCD-Grey Decoder Based on PAL

W = A + BD + BC

X = $B\bar{C}$

Y = B + C

Z = $\bar{A}\bar{B}\bar{C}D + BCD + A\bar{D} + B\bar{C}\bar{D}$

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- CPLD

- It is a programmable device with PLD as a macro unit and having a greater number of more complex gate circuits;
- Within the CPLD, a number of logical block macro cells similar to PAL and PLA are connected through a programmable connectivity and provide an external interface through a configurable I/O control block.

The diagram illustrates the architecture of the Altera MAX CPLD series in three parts:

- (a) **Chip flatness**: A top-down view of the chip showing a grid of Logical Array Blocks (LABs). An "Altera MAX" label is present at the bottom left.
- (b) **Logical array of blocks**: A detailed view of a single LAB. It shows the internal structure, including a Logic Array (LA), and labels such as "每个LAB内部有18个宏单元" (Each LAB contains 18 macro units).
- (c) **Macro unit internal logic**: A schematic diagram of the internal logic of a macro unit. It includes various logic gates, flip-flops (labeled DQ and M), and multiplexers. Inputs and outputs are labeled, along with specific logic functions like "可编程延迟" (Programmable delay) and "时序逻辑、组合逻辑、译码器" (Sequential logic, combinational logic, decoder).

Note: Figure (a) is the chip flatness, Figure (b) is a logical array of blocks (LAB), Figure (c) is the macro unit internal logic

Figure 3.20 The Architecture of Altera MAX CPLD Series

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– FPGA (Field Programmable Gate Array)

- Xilinx FPGA--Field Programmable Gate Array Based on RAM

The diagram illustrates the architecture of a Xilinx FPGA. It features a central grid of **Configurable Logic Blocks (CLBs)**, which are interconnected by a **Programmable Interconnect**. The interconnect consists of horizontal and vertical buses, each containing **CLBs** and **I/O Blocks (IOBs)**. The IOBs are connected to the external world via **I/O Pins**. The diagram also shows a detailed view of a CLB, which is composed of a **Configurable Logic Block (CLB)** and a **Configurable I/O Block (IOB)**. The CLB is further divided into a **Configurable Logic Block (CLB)** and a **Configurable I/O Block (IOB)**. The IOB is connected to the external world via **I/O Pins**. The diagram also shows a detailed view of a CLB, which is composed of a **Configurable Logic Block (CLB)** and a **Configurable I/O Block (IOB)**. The CLB is further divided into a **Configurable Logic Block (CLB)** and a **Configurable I/O Block (IOB)**. The IOB is connected to the external world via **I/O Pins**.

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• **Dynamic Partial Reconfigurable Logic (动态局部可重构逻辑)**

- The on-chip capacity increases, the wiring of the chip is more difficult, and the damage rate of FPGA increases;
- When logic is complex, it takes more time to reconstruct all of its circuits;
- In the case of other regional work, some logical areas can be reconstructed;

Local operations do not affect other logical operations during runtime;
 • Reduce the scale of circuit construction and improve the efficiency of construction;
 • It can flexibly support the dynamic changes in application requirements.

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• **Evolvable Hardware(EHW) vs. Reconfigurable Logic**

- Based on the evolutionary algorithm (genetic algorithm, genetic programming and evolutionary strategy) and the development of programmable logic

106

– **Application-Adaptive Logic Evolution**

- Application requirements are switched with the scene → Circuit logic needs to evolve
- Function Switching, Migration
- The Fault-Tolerant of Fault Logic
- Performance Optimization

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• **Polymorphic Computing vs. Reconfigurable Logic**

- Improve resource utilization, optimize calculating efficiency, and improve fault-tolerant capability, etc.
- (Improve Calculation Performance in Parallel)

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Adaptive Compute Acceleration Platform
自适应计算加速平台

- 自适应和智能计算的全球领先企业赛灵思公司 (Xilinx, Inc.), 2018 宣布推出一款超越 FPGA 功能的突破性新型产品 ACAP (Adaptive Compute Acceleration Platform, 自适应计算加速平台)。
- ACAP 是一个高度集成的多核异构计算平台, 能根据各种应用与工作负载的需求从硬件层对其进行灵活修改。可在工作过程中进行动态调节的自适应能力, 实现了 CPU 与 GPU 所无法企及的性能与性能功耗比。

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– **PCA: Polymorphic Computing Architecture**

DARPA

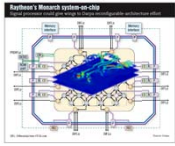

- U.S. DARPA: The static method lacks the diversity required for dynamic tasks, the performance degradation or poor performance of matching processing caused by this situation will ultimately damage our combat effectiveness. To reduce latency on a single chip, it has dynamic computing capability.
- Mission: Enable reactive systems that will reduce mission optimization and verification times from minutes to seconds. Develop processing architectures that adapt to mission requirements.

Key achievements: MIT "RAW", Stanford "Sm", UC Berkeley "TRIPS", UIUC/IBM "M3T" (tied to IBM "Gate"), ISI "MONARCH".

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- Raytheon MONARCH(Morphable Networked Micro-Architecture. 形变网络微架构)
 - The world's first adaptive polymorphic computer;
 - Multiple microprocessors form a computer array through a high degree of interconnection;
 - It can simultaneously optimize the foreground signal processing, background control and data processing;
 - High computing power, high data bandwidth, low power consumption, programmable.

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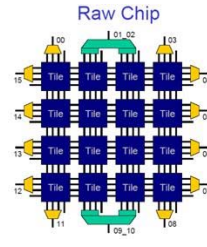
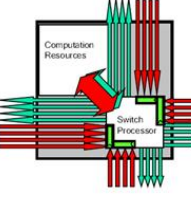
- The university of Texas has proposed a **trillionth computing reliable intelligent adaptive processing system TRIPS**
 - Using grid parallel processing and on-chip sensor system to achieve multi-state system ;
 - Each processing core has 16 isomorphic execution nodes and corresponding storage systems . Further more , it has an on-chip sensing system. The hardware can reconstruct itself according to different application software and different load, and realize data level parallelism, instruction level parallelism and thread Level parallel, which is called the polymorphic system;
 - The task of compiling and operating systems is new and complex.

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- The RAW Architecture of MIT
 - A RAW processor consists of 16 programmable Tiles. Each Tile has its own microprocessor, data cache, memory, and interconnected network interfaces that connect to each Tiles;


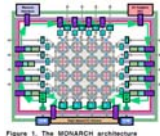



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- **High-Performance Polymorphic Embedded Hardware**
 - New Dynamic Local Reconfigurable FPGA Chip , Xilinx V5;
 - UC Berkeley's GARP research program, which aims to integrate reconfigurable computing units with common RISC processors into a single chip to improve processing power;
 - MIT's Professor Ethan *et al.* designed the on-chip reconfigurable structure MATRIX, its on-chip logical interconnect structure can be statically configured or dynamically switched;
 - CMU's Godstei *et al.* carried out the Reconfigurable Computing Research Program PipeRench, the design introduces a pipeline reconstruction technology;

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- The university of Texas released a multiform microprocessor architecture called Trips(A reliable intelligent adaptive processing system for trillions of calculations per second);
- Nvidia uses the Kepler architecture in the new GPGPU to support the dynamic allocation of the kernel resources and the construction of architecture.

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- **Many-Core Processor and Parallel Computation Technique**

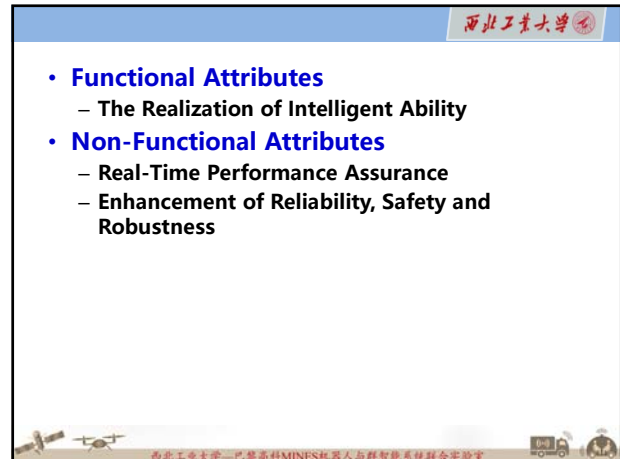


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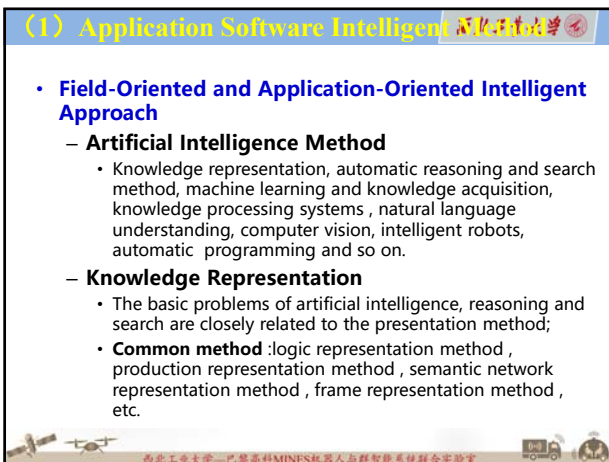
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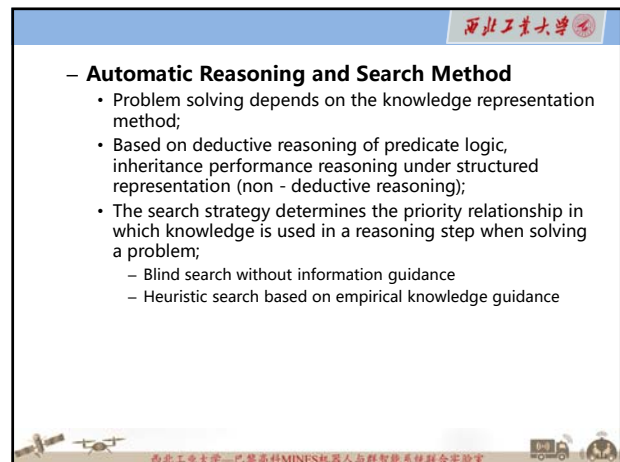
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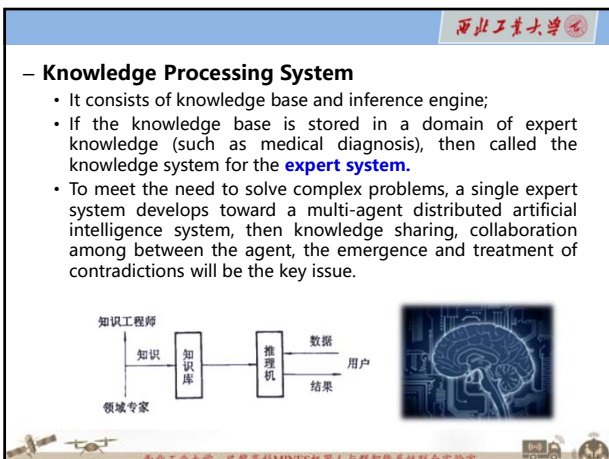
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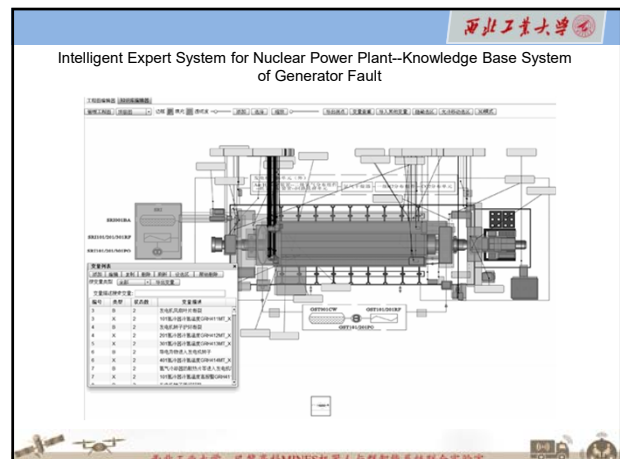
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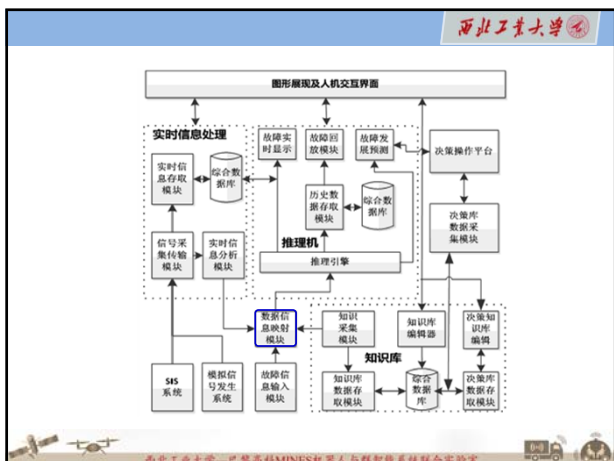
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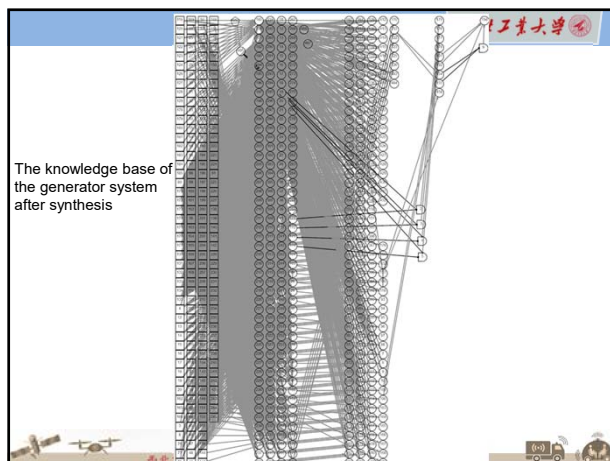
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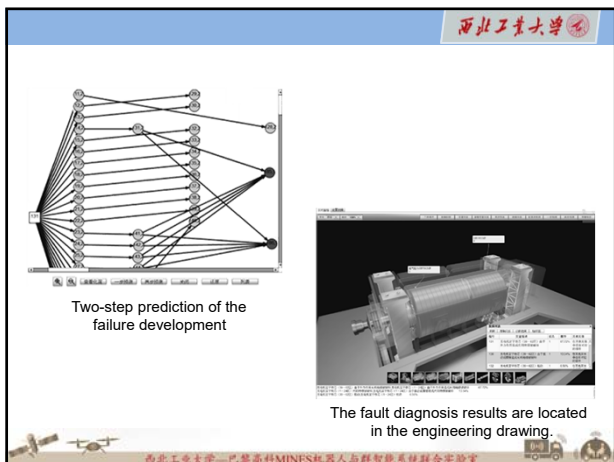
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Machine Learning

– **Machine Learning** Another important subject of artificial intelligence;

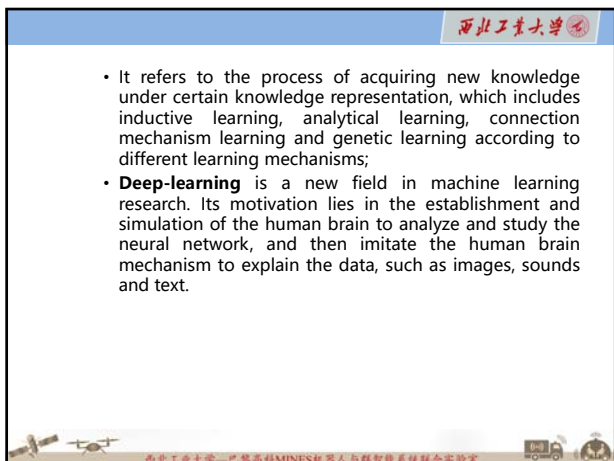
- Machines produce **self-learning** , just as : If you put the child in the car, let him observe the outside world from the window, he will see a variety of vehicles, and no one tell him what these objects are. He is through their own observation to summarize the similar things.
- Alan Turing(艾伦·图灵), the father of computer science, defined the artificial intelligence in 1950 as follows : **Suppose you are chatting with some entity via a text chat session. Could you tell, solely from the conduct of the session, whether the other entity was a human or a machine ? If not, then that entity is judged to be intelligent.** **The fresh idea of machine learning** is to let the machine think like the human brain.



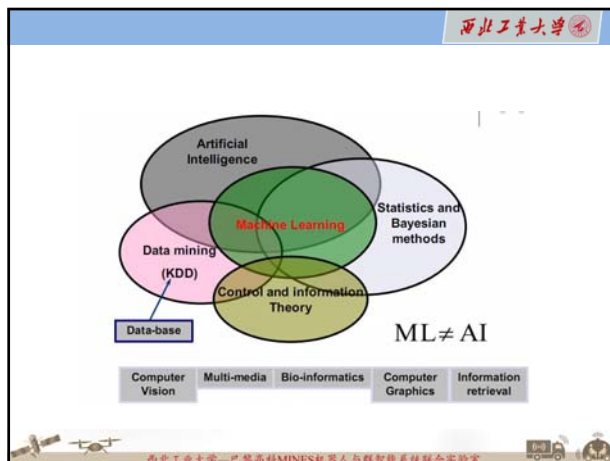
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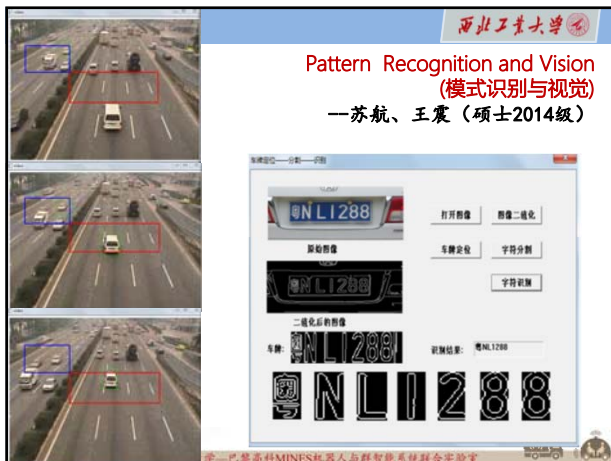
- It refers to the process of acquiring new knowledge under certain knowledge representation, which includes inductive learning, analytical learning, connection mechanism learning and genetic learning according to different learning mechanisms;
- **Deep-learning** is a new field in machine learning research. Its motivation lies in the establishment and simulation of the human brain to analyze and study the neural network, and then imitate the human brain mechanism to explain the data, such as images, sounds and text.



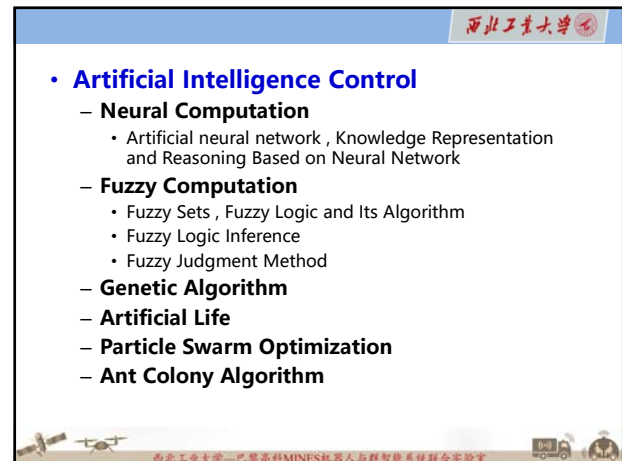
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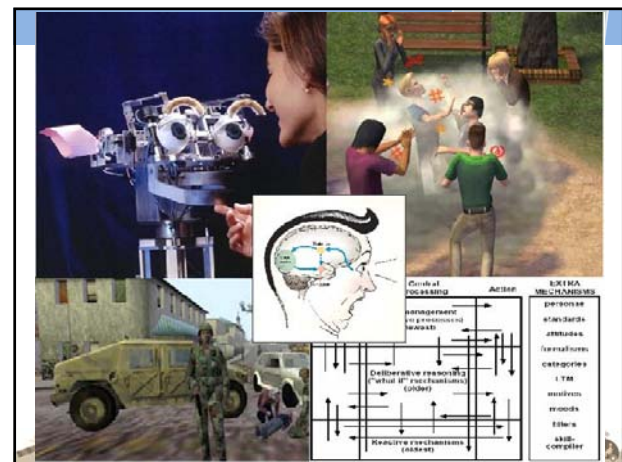
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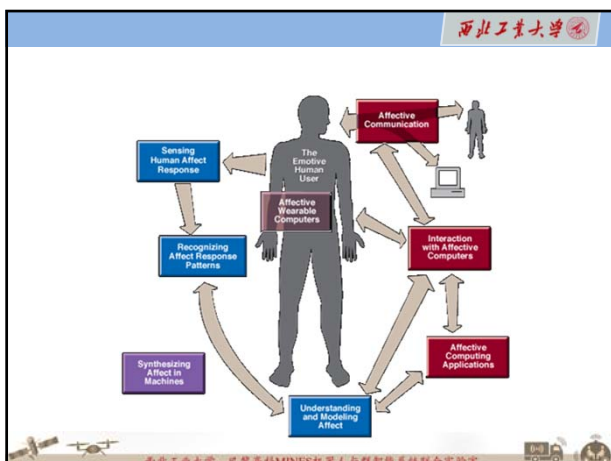
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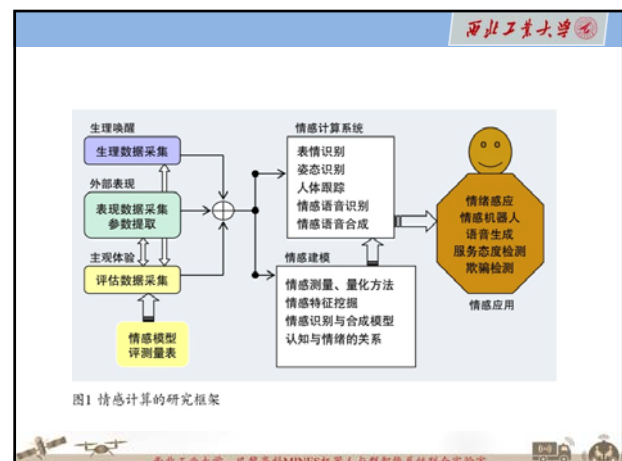
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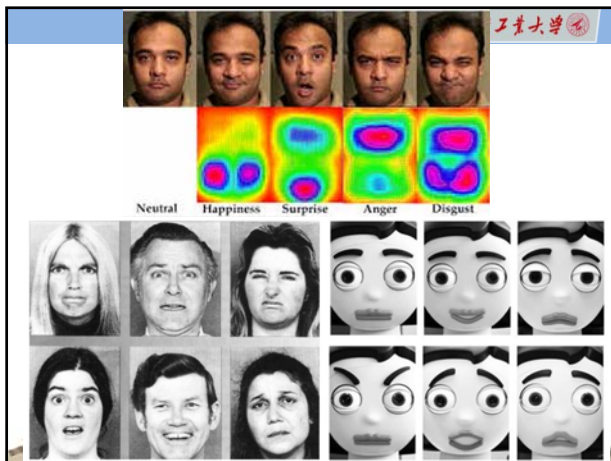
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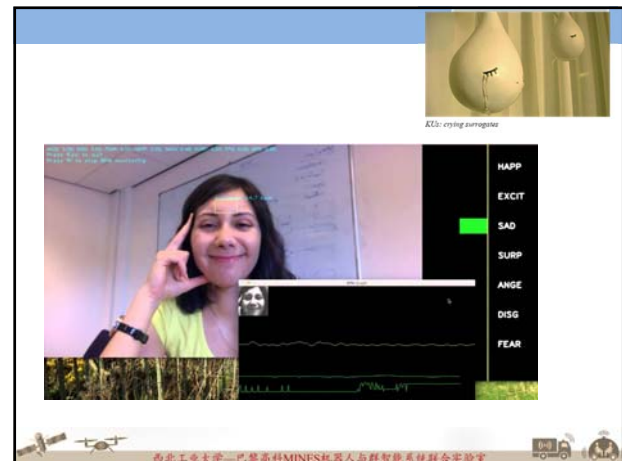
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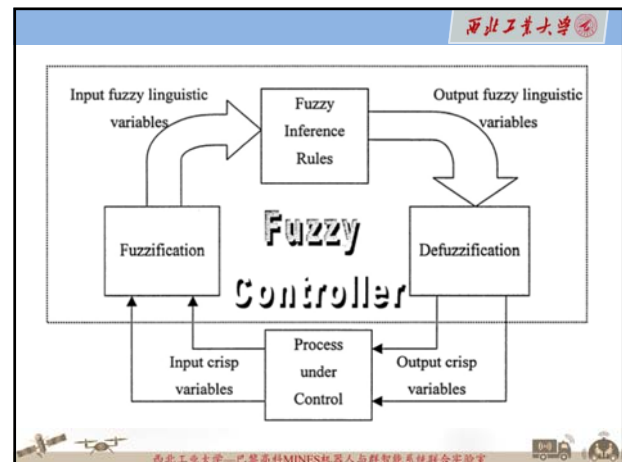
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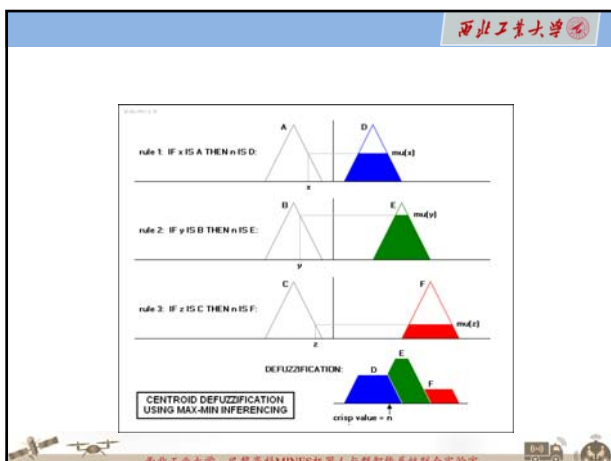
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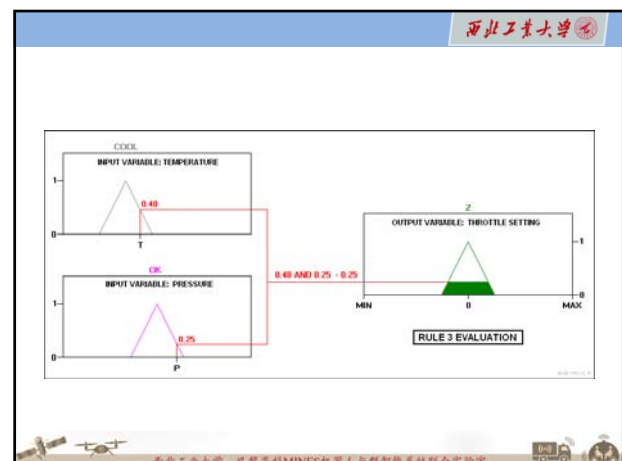
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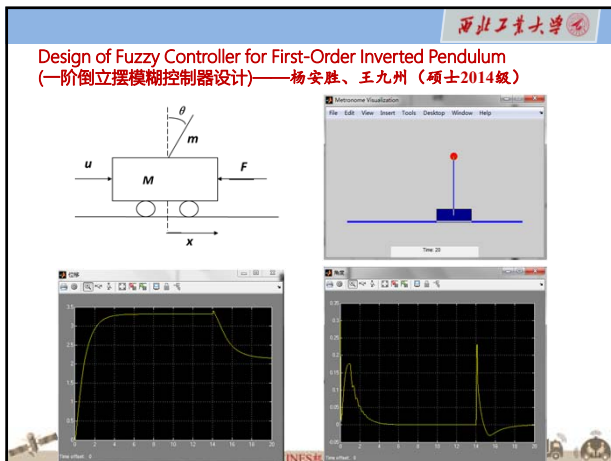
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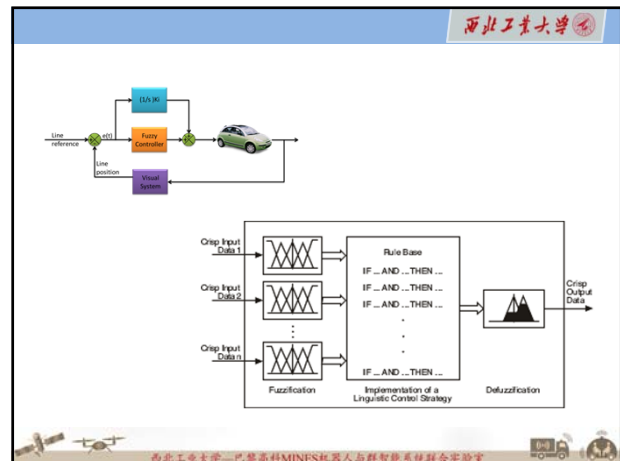
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– The Evolution of Fuzzy Control

- Fuzzy-PID Compound Control
- Adaptive Fuzzy Control
- Parameters Self-Tuning Fuzzy Control
- Expert Fuzzy Controller(专家模糊控制EFC)
- Human Simulating Intelligent Fuzzy Control
- Neuro-Fuzzy Control(神经模糊控制)
- Multivariable Fuzzy Control

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• Artificial Neural Network(ANN,神经网络)

– A mathematical model or a computational model that mimics the structure and function of a biological neural network, a nonlinear statistical data modeling tool, commonly used to model the complex relationships between input and output;

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– The ANN is formed by the massive nodes (or neuron) and the connection constitution, each node represents a specific output function (activation function), and the connection between each of the two nodes represents a weighting value for the signal passing through the connection, called the weight, which corresponds to the memory of the artificial neural network.

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数学表示 $t = f(\vec{W}\vec{A}' + b)$

- \vec{W} 为权向量
- \vec{A} 为输入向量, \vec{A}' 为 \vec{A} 的转置
- b 为偏置
- f 为传递函数

• $a_1 \sim a_n$: the input vectors of various components;

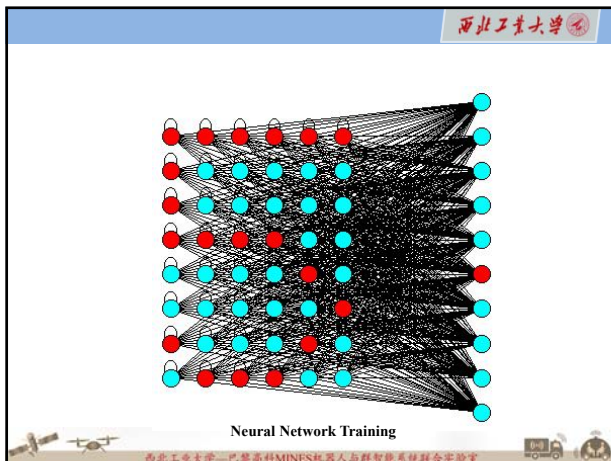
• $w_1 \sim w_n$: the weight of each synapse of neurons;

• B : bias;

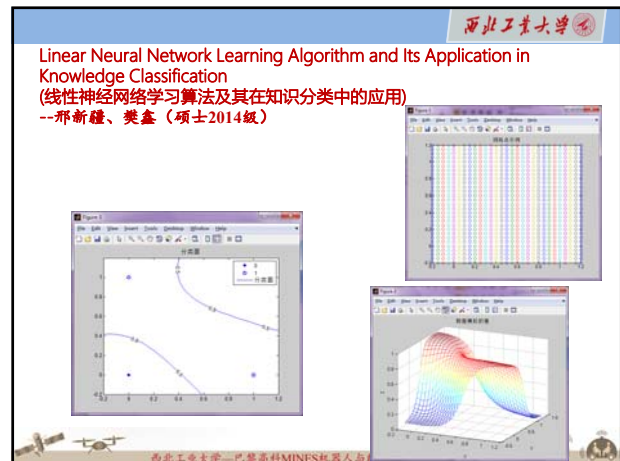
• F : transfer functions, usually a nonlinear function, generally there are `traingd()`, `tansig()`, `hardlim()`;

• t : the output of neurons

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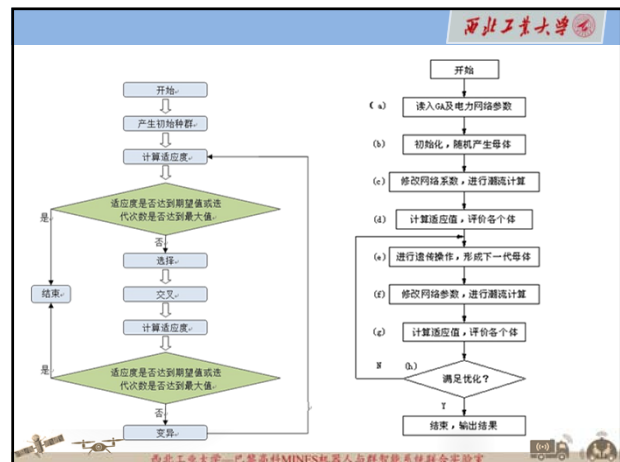


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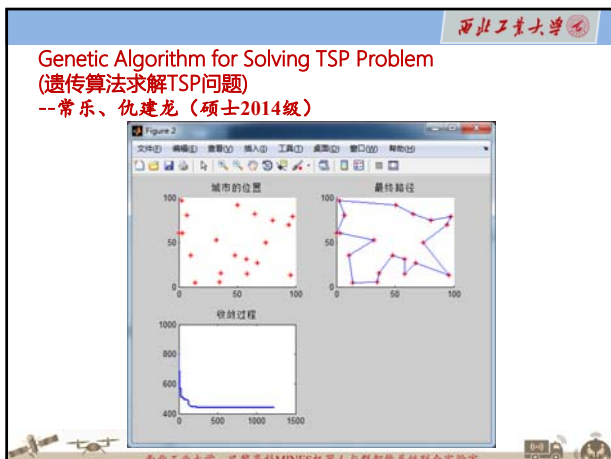
Evolutionary Computation

- It covers successive optimization, combinatorial optimization and so on. It is a global optimization method;
- It is a general term of genetic algorithm, evolutionary strategies and evolutionary programming;
- Mainly used in engineering control, machine learning, function optimization and other fields;

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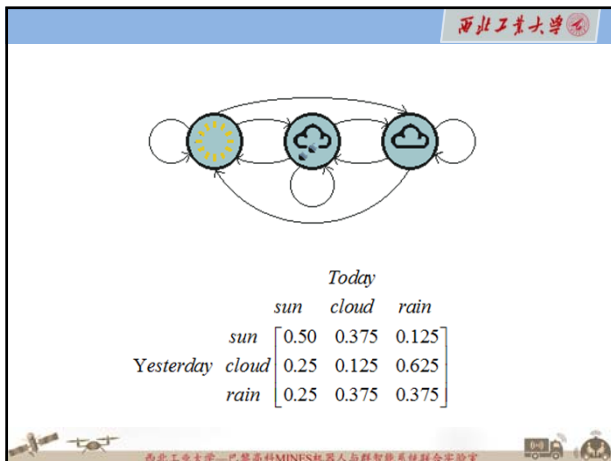
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Markov Chain and Its Model

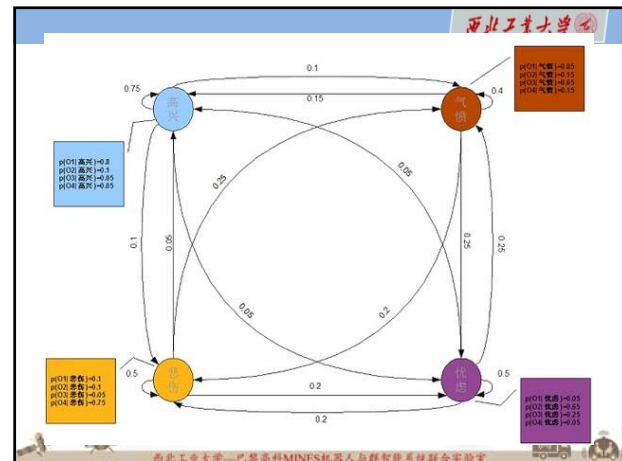
- When given the current state, it is conditionally independent of the past state (i.e. the historical path of the process), then the stochastic process has Markov property.
- The Markov process with a discrete state is usually called the Markov Chain.
- Markov chain describes a state sequence whose every state value depends on the previous finite state. The statistical model is used to describe a Markov process with implied unknown parameters;
- In the case of a given current knowledge or information, the historical state of the past is irrelevant to predict the future (i.e. the future state after the present period);
- It is often used to model queuing theory and statistical modeling, and can also be used as signal models for entropy coding techniques, such as algorithmic coding.

$$P(X_{n+1} = x | X_1 = x_1, X_2 = x_2, \dots, X_n = x_n) = P(X_{n+1} = x | X_n = x_n).$$

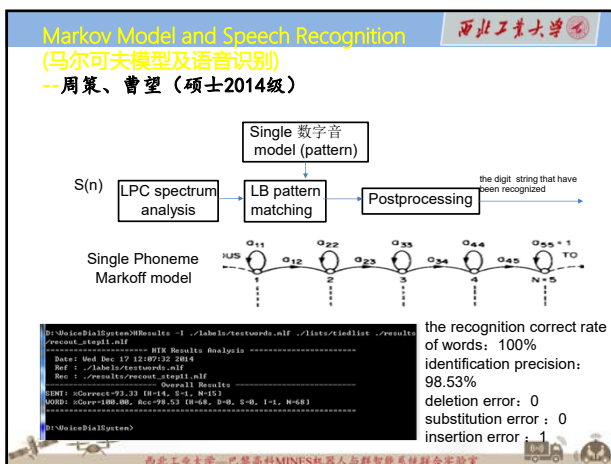
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Method	Characteristics	Typical Applications
ANN(人工神经网络)	Unsupervised or supervised (trained) learning, black-box data processing structure;	Signal calibration, pattern recognition, classification;
AFS(模糊神经网络)	Integration of Fuzzy inference rules with neural networks;	Intelligent control, embedded expert system
MAS(多智能体系统)	Abstract for multi-agent network, different aspects of cooperation methods are studied according to the application;	Multi-agent system collaboration, Such as multi-robot, unmanned vehicles, intelligent production line;
PCA(多形态计算)	Flexible dynamic architecture and resource management mechanism, dynamic matching of resources and computing hotspots;	Construction of a high-performance adaptive embedded computing system for new complex intelligent systems;

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Method	Characteristics	Typical Applications
GA(遗传计算)	Optimization method especially for large and unstructured search space;	Real-time scheduling, program timing analysis;
MC(模型验证)	Automatic attribute verification method for complex systems;	Analysis and verification of system function and non function attribute;

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Method	Characteristics	Sample embedded applications
Artificial Neural Networks	unsupervised or supervised (trained) learning, black-box data processing structure	signal calibration [5], pattern recognition [26], classification [7]
Genetic Algorithms	optimization method especially for large and unstructured search space	real-time scheduling [27], program timing analysis [8]
Model Checking	automated method for verifying finite state concurrent systems	verifying properties of distributed systems [25]
Multi-Agent Systems	distributed system of self-acting programs acting in a network	flexible automated manufacturing [14]
Neural Fuzzy Systems	integration of Fuzzy inference rules with neural networks	intelligent control [28], embedded expert systems [29]

Table 1: Overview on presented methods

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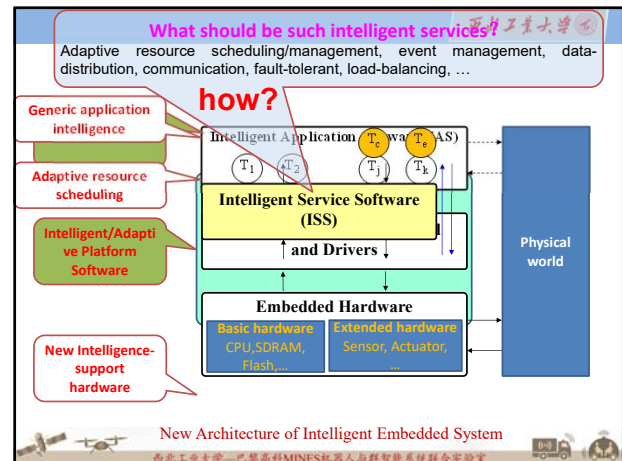
(2) Intelligent Methods for *System Software*

You remember this slide? remember OODA loop? PCA?...

If at different stage, different situation, only require different functions or performance, how to weigh the balance between resource supply and the weight, power, performance?

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西北工业大学

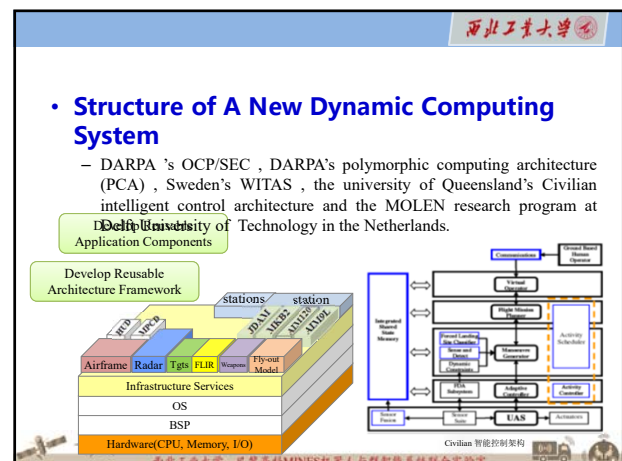
An Evaluation of User Satisfaction Driven Scheduling in a Polymorphic Embedded System

A polymorphic system consists of heterogeneous cores such as CPU, GPU, FPGA, and ASIC cores. A **polymorphic thread** is compiled for multiple morphisms afforded by these diverse cores. The resulting polymorphic computing system can solve two problems - (1) **Polymorphic threads enable more complex, dynamic trade-offs between delay and power consumption.** A piecewise cobbling of multiple morphism energy-delay profiles of individual thread morphisms offers a richer energy-delay profile for the entire application. (2) **The OS scheduler not only picks a thread to run, it also chooses the thread's morphism.**

In this work, we propose a scheduler to optimize a class of User Satisfaction Index (USI) functions. We develop a model for a mobile polymorphic embedded system computing platform. We integrate a polymorphic scheduler in this model to assess the application design space offered by polymorphic computing... We further discuss the feasibility of USI-based polymorphic scheduler by identifying its strengths and weaknesses in relation to the application design space based on the simulation results.

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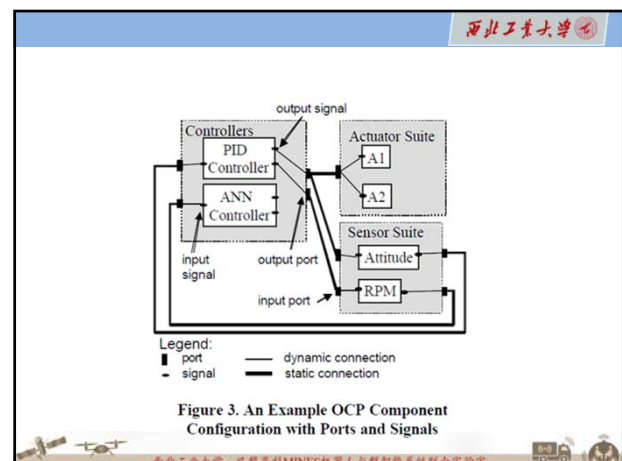
SEC DARPA & AFRL **DARPA**

• OCP: Open Control Platform

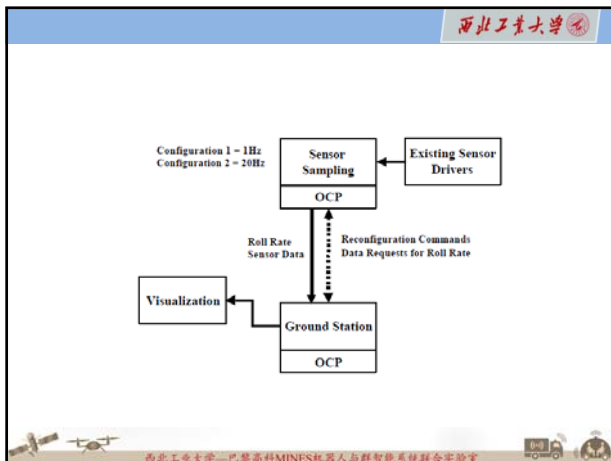
- Software Enabled Control Project(SEC) for Intelligent UAV
- OCP provides an open, middleware-based software framework and a development platform. And provides the runtime methods, software, and simulation capabilities of multiprocessors and aircraft.
- Based on the real-time CORBA middleware, it makes the application have nothing to do with the hardware, provides the control of the executing component, the communication between components, and the distribution and deployment of application components in the form of services;
- The ultimate goal is to create a common environment that can be used for large-scale control system problems.

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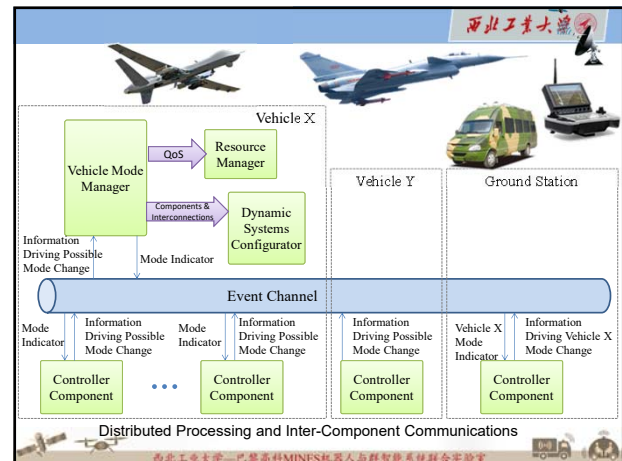
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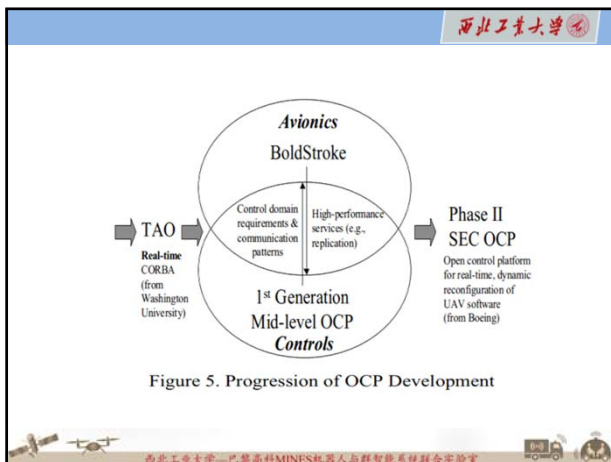
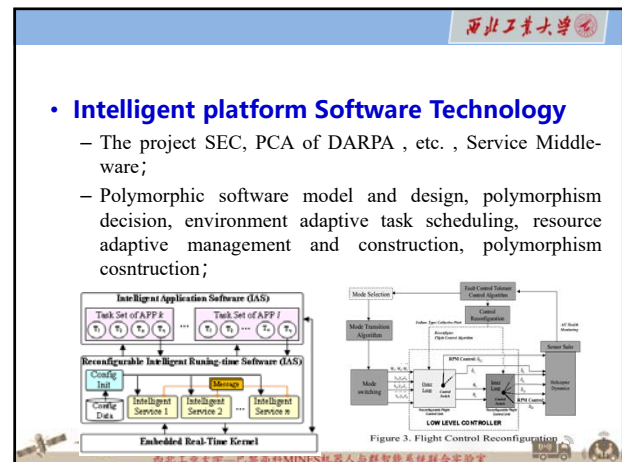
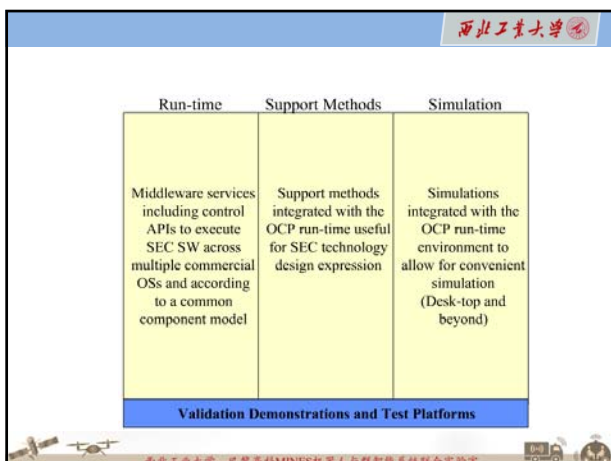


Figure 5. Progression of OCP Development

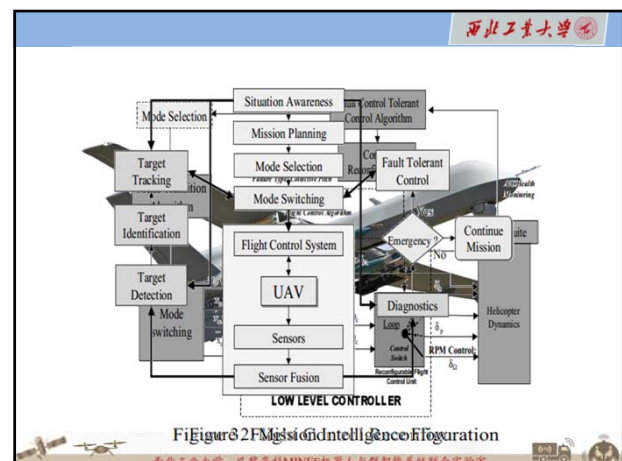
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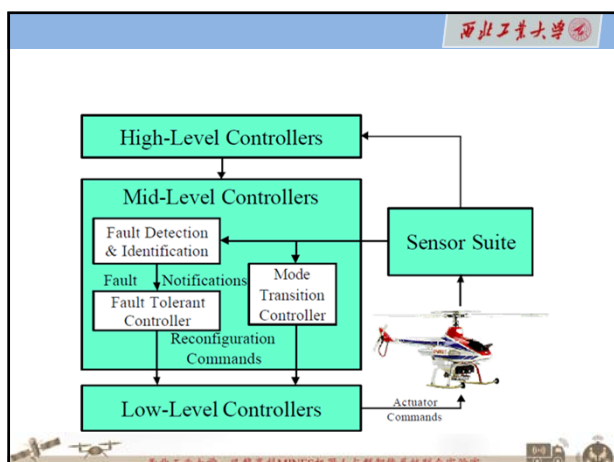
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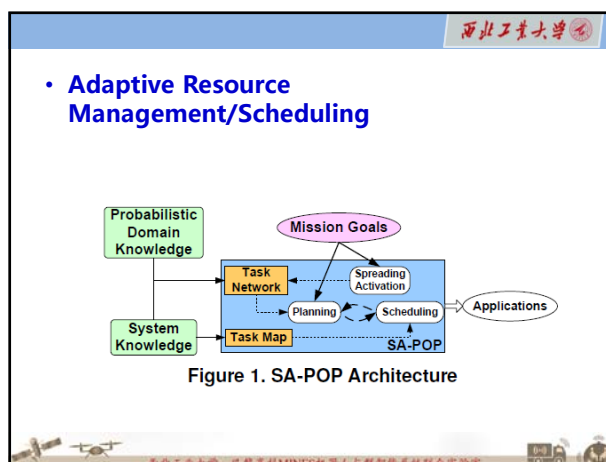
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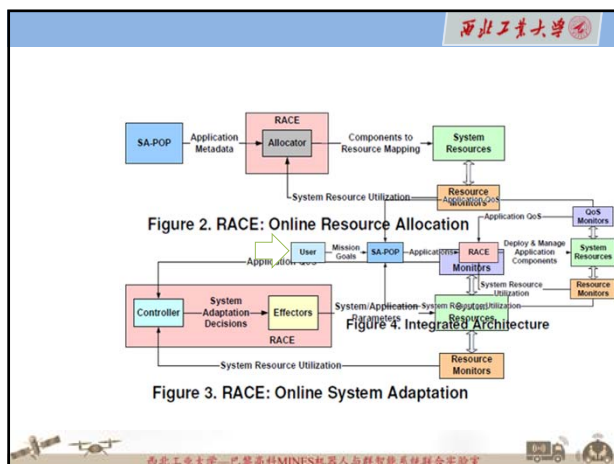
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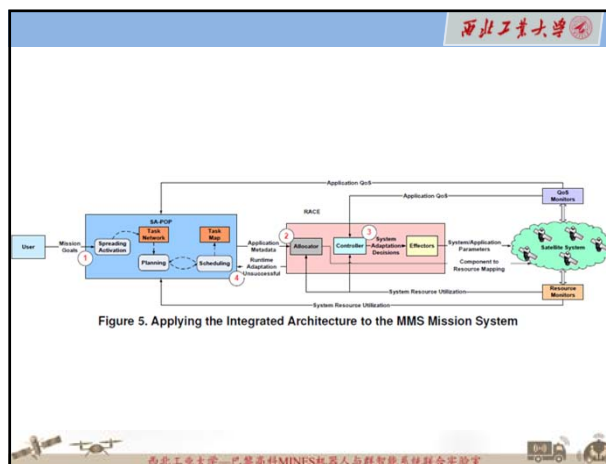
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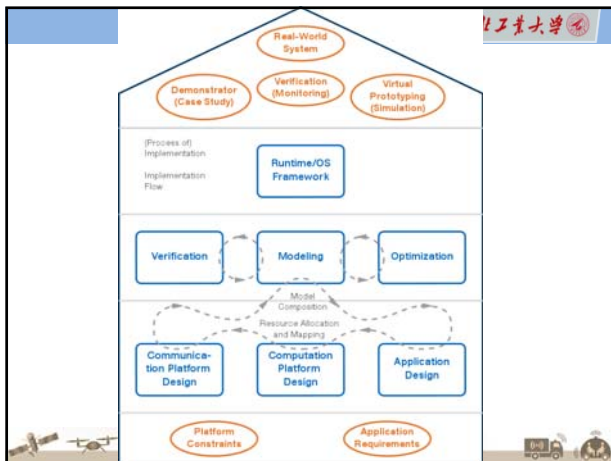
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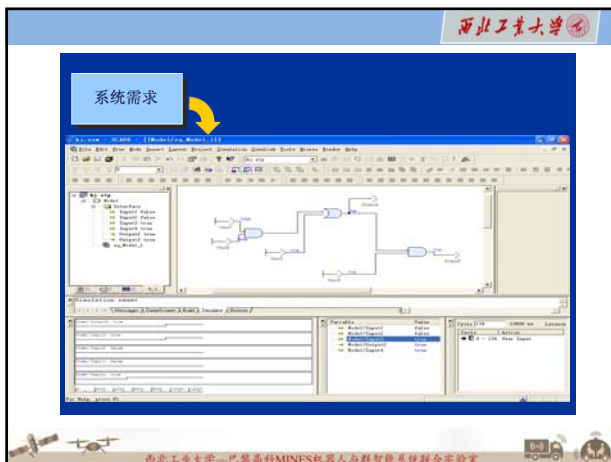
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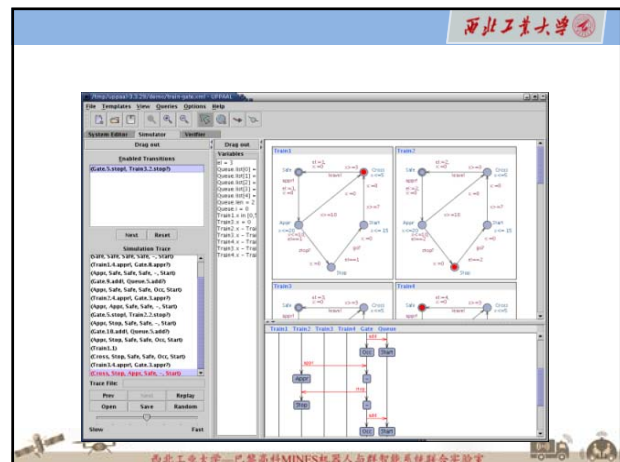
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- *Synchronous programming languages*
- STATEMATE, Rhapsody (ilogix)
- Rose RT (Rational)
- SCADE, Esterel Studio (Esterel Technologies)
- Stateflow and Simulink (Mathworks)
- **Timed Automata**
- UPPAAL Verification Tool
- POLIS (Berkeley), Cierito VCC (Cadence)
- SpecCharts (Irvine)

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– System Design and Verification Method

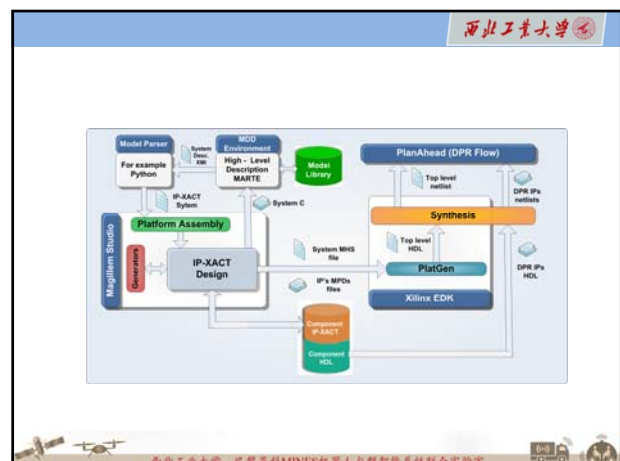
- Model system design theory, methods and tools;
- Real-time UML extension —MARTE profile, the UML Gaspard profile of Inria and university of Burgundy, which is modeling for reconfigurable system, Real-time UML profile of TUT university in Finland;
- The Lockheed Martin Company studies the methods of verification and validation of PCA and verifies it on avionics. The authentication environment READAPT has been developed at present.

Version 00171117, version 2

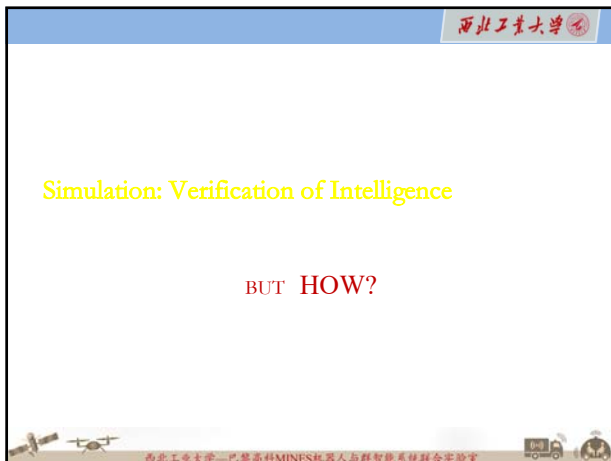
Gaspard2 UML profile documentation

Rabie Ben Attallah¹, Pierre Boulet⁽⁵⁰⁾, Arnaud Cuccini¹, Jean-Luc Dekeyser^{1,2}, Antoine Honoré¹, Ouassila Labbani^{1,2}, Sébastien Le Beux¹, Philippe Marquet^{1,2}, Eric Piet⁽⁵⁰⁾, Julien Taitard¹, Huiling Yu¹

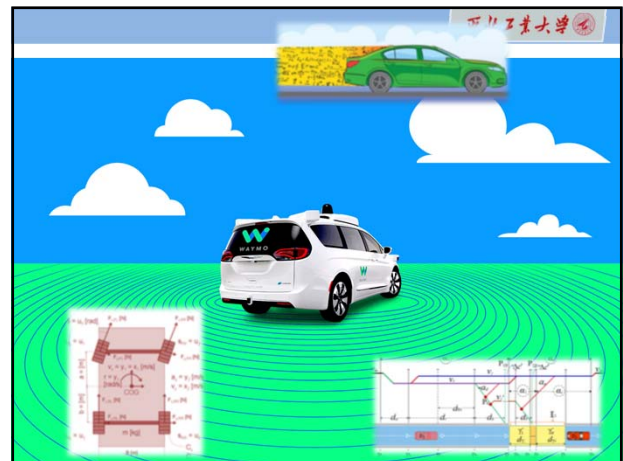
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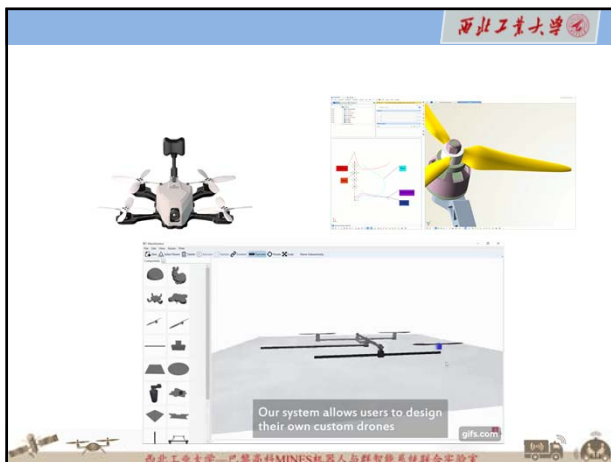
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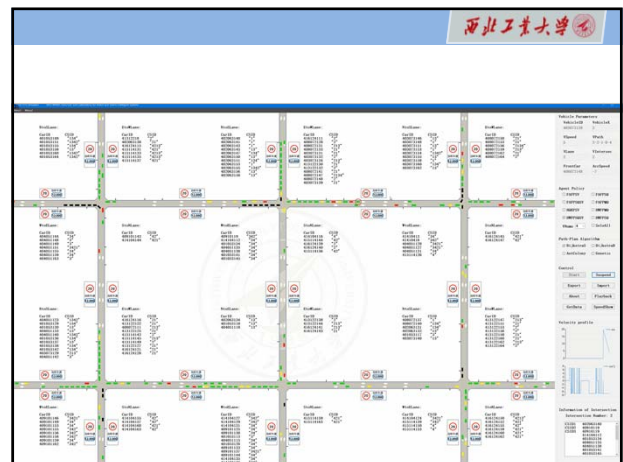
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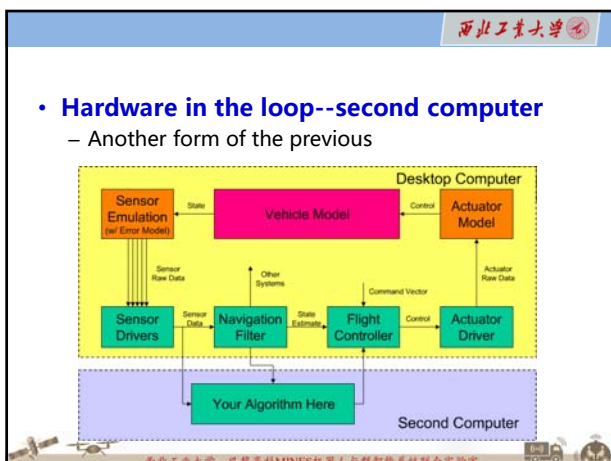
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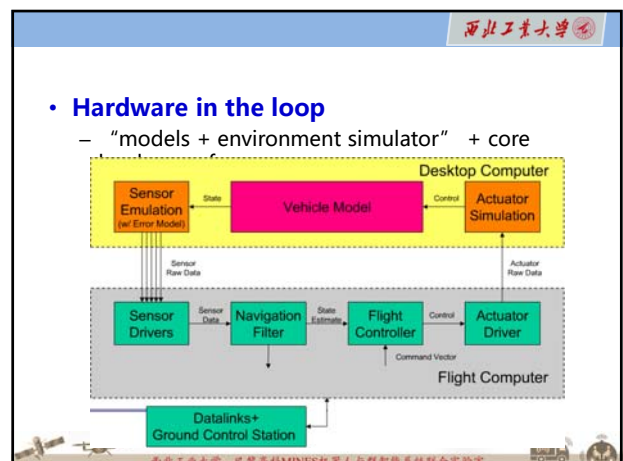
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How to TEST & TRAIN intelligent vehicles?

The fact:
in the last 9 years,
Waymo has been tested in virtual environment, 5 billions miles!





Test and train in physical world!
Google's Waymo has travelled more than 5 millions miles.
If problems? & how?

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How to TEST & TRAIN intelligent vehicles?

- **Virtual environment** and **Big data** based test and training is one vital way to verify and train intelligent vehicles!
- **Typical tools**
 - DRIVE PX
 - Dominik Dörr
 - Roberto De Vecchi
 - Pro-SiVIC
 - Pro-SiVIC
 - ...

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How to TEST & TRAIN intelligent vehicles?

- **Virtual environment** and **Big data** based test and training is one vital way to verify and train intelligent vehicles!






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Typical example: UAV + Simulated environment



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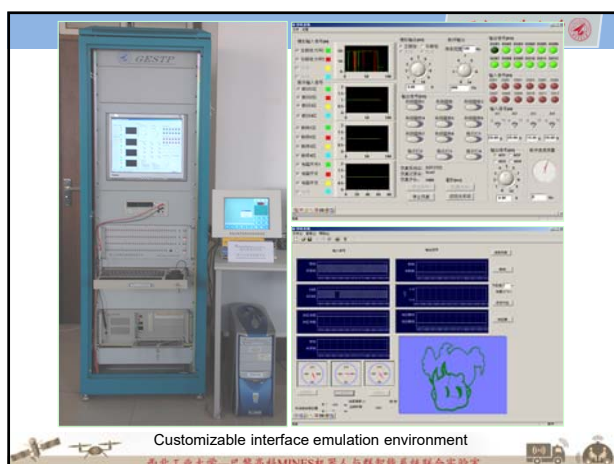



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Self-Developed Embedded Software and Systems




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